POLOTEC, INC.
MBA-503B

Processor	80386DX/CX486DLC
Processor Speed	25/33/40MHz
Chip Set	Micronics
Max. Onboard DRAM	32MB
SRAM Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387/3167
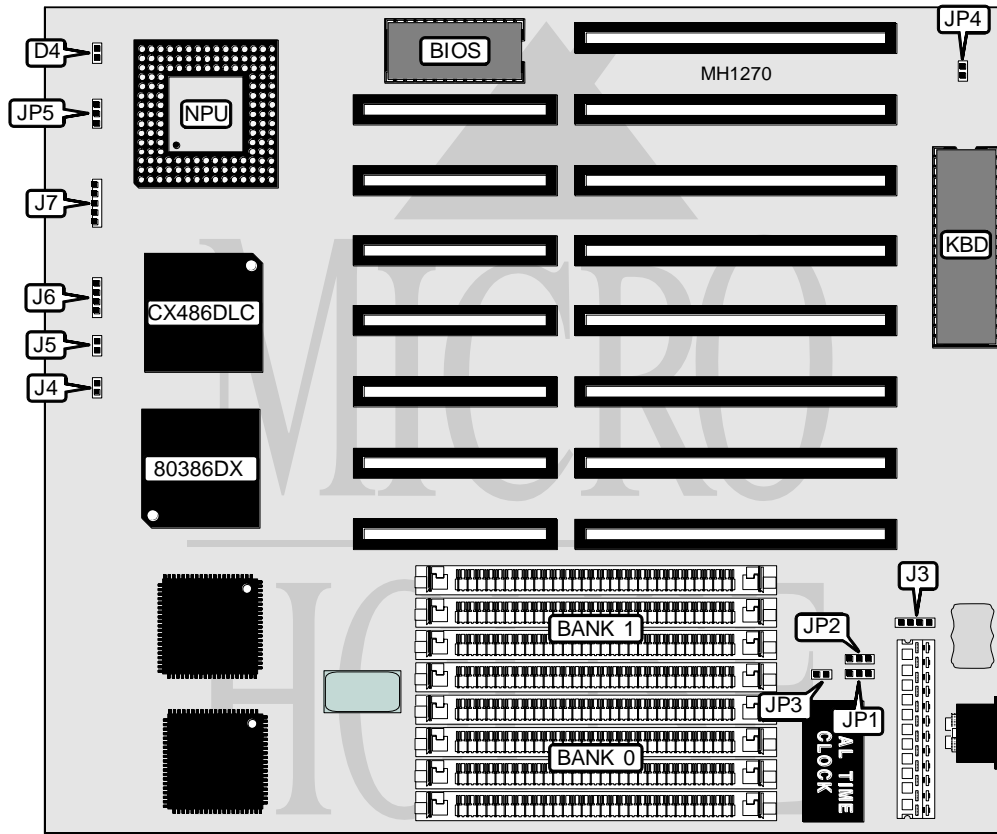
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	D4	Reset switch	J5
External battery	J3	Speaker	J6
Parallel port	J4	Power LED & keylock	J7

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í RTC type select 146818	JP1	pins 2 & 3 closed
	JP3	closed
RTC type select DS1287	JP1	pins 1 & 2 closed
	JP3	open
í CMOS memory normal operation	JP2	pins 1 & 2 closed
CMOS memory clear	JP2	pins 2 & 3 closed
í Monitor type select color	JP4	closed
Monitor type select monochrome	JP4	open
NPU select 80387	JP5	pins 1 & 2 closed
NPU select 3167 or none	JP5	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9