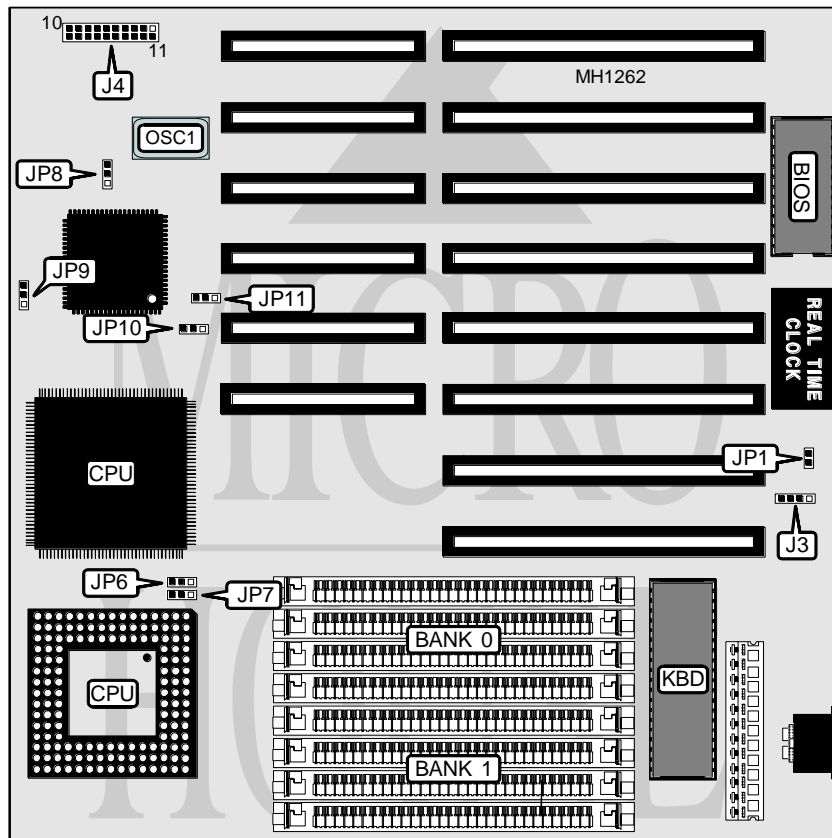


ACER, INC.

ISA486GSI MODEL IP25A

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/50(internal)/66(internal)
Chip Set	ALI
Max. Onboard DRAM	32MB
SRAM Cache	None
BIOS	Acer
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Reset switch	J4/11 & 12
Power LED & keylock	J4/1 - 5	Turbo switch	J4/14 - 16
Speaker	J4/7 - 10	Turbo LED	J4/18 & 19

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ISA486GSI MODEL IP25A

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	open
CMOS memory clear	JP1	closed
í Battery select internal	J3	pins 2 & 3 closed
Battery select external	J3	connected

CPU TYPE CONFIGURATION		
CPU	JP6	JP7
80486DX2	pins 2 & 3 closed	pins 2 & 3 closed
ODP486SX	open	pins 1 & 2 closed
80486DX	pins 2 & 3 closed	pins 2 & 3 closed
80487SX	pins 1 & 2 closed	pins 1 & 2 closed
80486SX	open	pins 1 & 2 closed

CPU SPEED CONFIGURATION					
CPU Speed	OSC1	JP8	JP9	JP10	JP11
20MHz	40MHz	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 2 & 3
25MHz	50MHz	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 2 & 3
33MHz	66MHz	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 2 & 3
50MHz	50MHz	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 2 & 3
60MHz	66MHz	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 2 & 3

Note: Pins designated should be in the closed position.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9