INFORMTECH INTERNATIONAL, INC.
B A B Y   2 8 6 M B   V E R .   1 . 2 0

Processor	80286
Processor Speed	5/6/8/10MHz
Chip Set	C & T
Max. Onboard DRAM	1MB
SRAM Cache	None
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	Auxilliary keyboard connector
NPU Options	80287

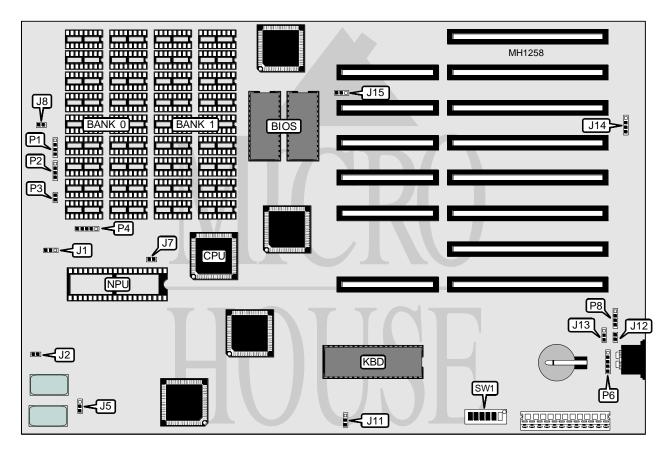| USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Power good signal detect from power supply	J14	pins 2 & 3 closed
Power good signal detect from board	J14	pins 1 & 2 closed
BIOS type select 27256	J15	pins 1 & 2 closed
BIOS type select 27128	J15	pins 2 & 3 closed
í Factory configured - do not alter	SW1/Switch 5	closed
í Monitor type select monochrome	SW1/Switch 6	open
Monitor type select color	SW1/Switch 6	closed

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BABY 286MB VER. 1.20

CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	P1	Power LED & keylock	P4
Turbo LED	P2	Auxilliary keyboard	P6
Reset switch	P3	External battery	P8

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	SW1/Switch 3	SW2/Switch 4
256KB	(18) 4164	(18) 4164	closed	open
512KB	(18) 41256	NONE	open	open
í 640KB	(18) 41256	(18) 4164	closed	closed
1024KB	(18) 41256	(18) 41256	open	closed

NPU SPEED CONFIGUARATION			
NPU Speed	Clock Rate	J7	J8
80287-12	8/12MHz	open	pins 1 & 2 closed
80287-12	6.7/10MHz	open	pins 1 & 2 closed
80287-10	6.7/10MHz	open	pins 1 & 2 closed
80287-10	5.3/8MHz	closed	pins 2 & 3 closed
80287-8	5.3/8MHz	closed	pins 2 & 3 closed
80287-8	4.4/6MHz	closed	pins 2 & 3 closed
80287-6	4.4/6MHz	closed	pins 2 & 3 closed
80287-6	2.6/4MHz	closed	pins 1 & 2 closed

BATTERY TYPE CONFIGUARATION		
Battery	J12	J13
í Internal lithium (2 x 3VDC)	pins 1 & 2 closed	closed
External lithium (6VDC)	open	closed
External alkaline (6VDC)	open	open

SYSTEM CLOCK WAIT STATE CONFIGURATION					
Speed	Wait State	J2	J5	J8	J11
10MHz	0	closed	pins 1 & 2	closed	pins 1 & 2
10MHz	1	open	pins 1 & 2	open	pins 1 & 2
12MHz	0	closed	pins 2 & 3	closed	pins 2 & 3
12MHz	1	open	pins 2 & 3	open	pins 2 & 3
Note: Pins designated should be in the closed position.					

I/O MODE WAIT STATE CONFIGURATION					
Wait States	8-BIT I/O	8-BIT Memory	16-BIT I/O	16-BIT Memory	SW1/ Switch 1	SW1/ Switch 2
0	0 (6)	0 (6)	0 (3)	0 (3)	closed	closed
1	0 (6)	0 (6)	0 (3)	1 (4)	closed	open
2	1 (7)	1 (7)	1 (4)	1 (4)	open	closed
3	2 (8)	2 (8)	1 (4)	2 (5)	open	open