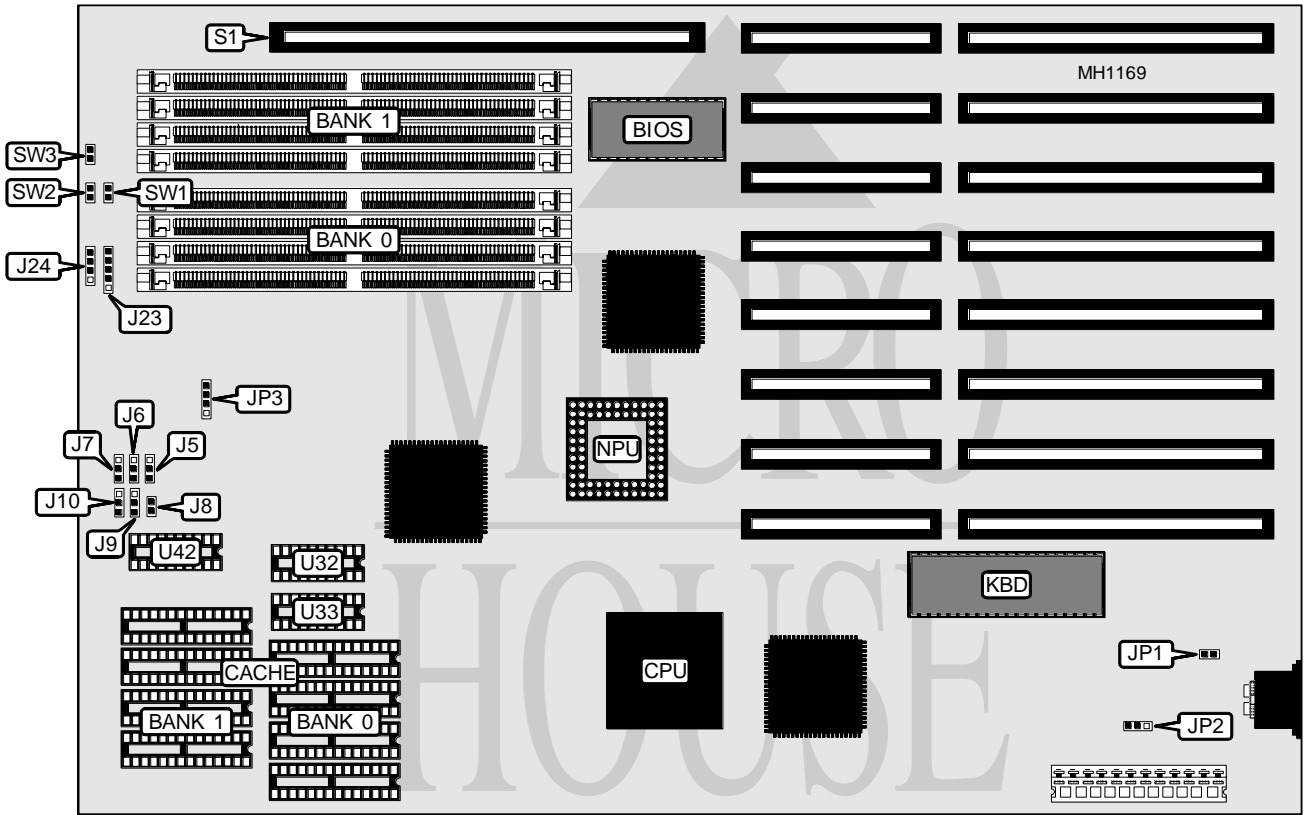


DASH COMPUTER, INC. DC 386 WB WRITE BACK-CACHE

Processor	80386DX
Processor Speed	25/33/40MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot
NPU Options	80387



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J23	Reset switch	SW1
Speaker	J24	Turbo switch	SW2
32-bit external memory card	S1	Turbo LED	SW3

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DC 386 WB WRITE BACK-CACHE

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP1	open
Monitor type select color	JP1	closed
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Bus speed select ATCLK/6	JP3	pins 1 & 2 closed
Bus speed select ATCLK/8	JP3	open
Note: Pins 3 and 4 on JP3 are not used		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE JUMPER CONFIGURATION						
Size	JP5	JP6	JP7	JP8	JP9	JP10
32KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	open	pins 2 & 3	pins 2 & 3
64KB	pins 2 & 3	pins 1 & 2	pins 1 & 2	open	pins 2 & 3	pins 1 & 2
128KB	pins 2 & 3	pins 1 & 2	pins 2 & 3	closed	pins 2 & 3	pins 1 & 2
256KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	closed	pins 1 & 2	pins 1 & 2
Note: Pins designated should be in the closed position						

CACHE CONFIGURATION				
Size	Cache SRAM	Location	TAG (U32 & U33)	Dirty Bit (U42)
32KB	(4) 8K x 8	Bank 0	(2) 4K x 4	(1) 4K x 4
64KB	(8) 8K x 8	Banks 0 & 1	(2) 4K x 4	(1) 4K x 4
128KB	(4) 32K x 8	Bank 0	(2) 16K x 4	(1) 16K x 1
256KB	(8) 32K x 8	Banks 0 & 1	(2) 16K x 4	(1) 16K x 1