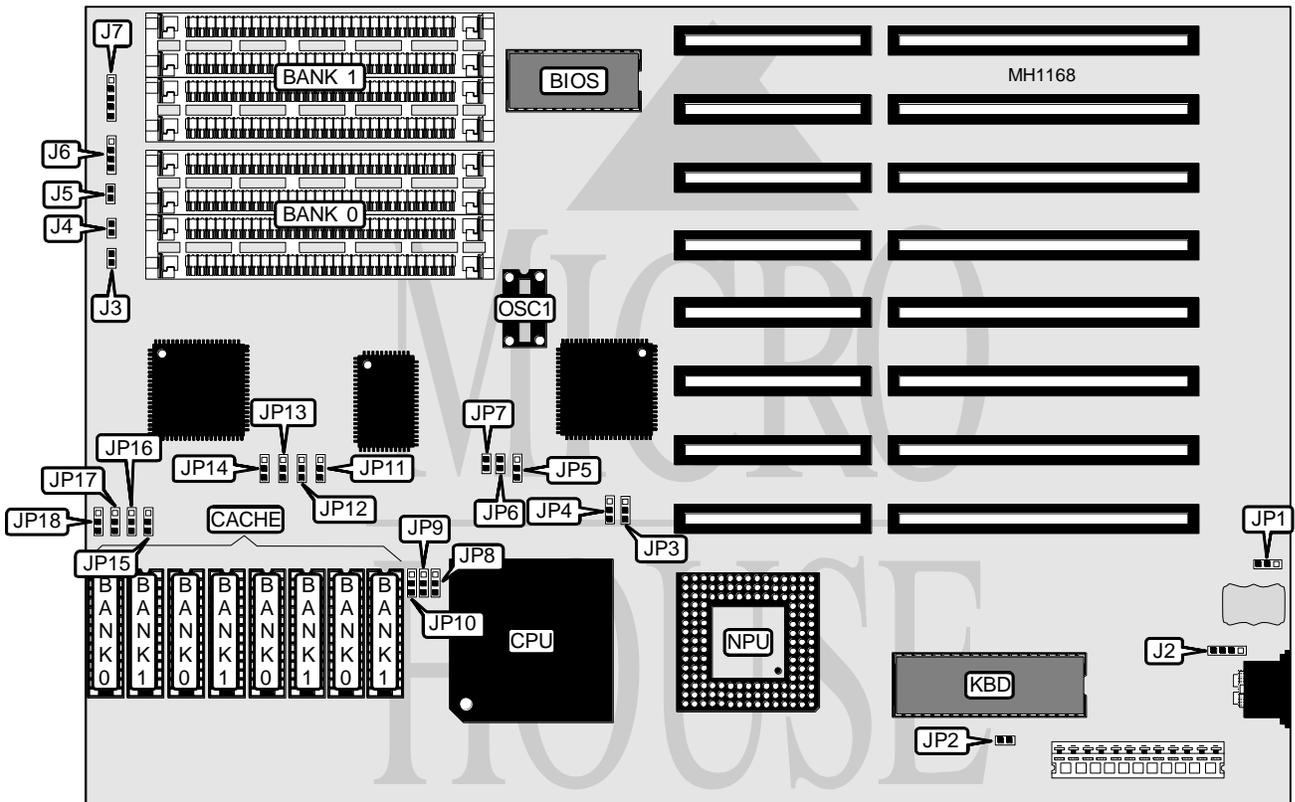# UNIDENTIFIED
80486 DX CACHE

Processor   80486SX/80487SX/80486DX
Processor Speed   16/20/25/33MHz
Chip Set   Symphony
Max. Onboard DRAM   32MB
SRAM Cache   64/128/256KB
BIOS   AMI/DTK
Dimensions   330.2mm x 218.4mm
I/O Options   None
NPU Options   4167

CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Reset switch	J5
Turbo switch	J3	Speaker	J6
Turbo LED	J4	Power LED & keylock	J7

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 1 & 2 closed
CMOS memory clear	JP1	pins 2 & 3 closed
í Monitor type select monochrome	JP2	open
Monitor type select color	JP2	closed

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UNIDENTIFIED
80486 DX CACHE

CPU CONFIGURATION			
CPU	JP8	JP9	JP10
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

SRAM JUMPER CONFIGURATION			
Jumper	64KB	128KB	256KB
JP3	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
JP4	pins 1 & 2 closed	open	pins 2 & 3 closed
JP5	pins 2 & 3 closed	open	pins 2 & 3 closed
JP6	open	closed	open
JP7	open	closed	open
JP11	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
JP12	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
JP13	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
JP14	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
JP15	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
JP16	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed
JP17	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed
JP18	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed

SRAM CONFIGURATION		
Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8