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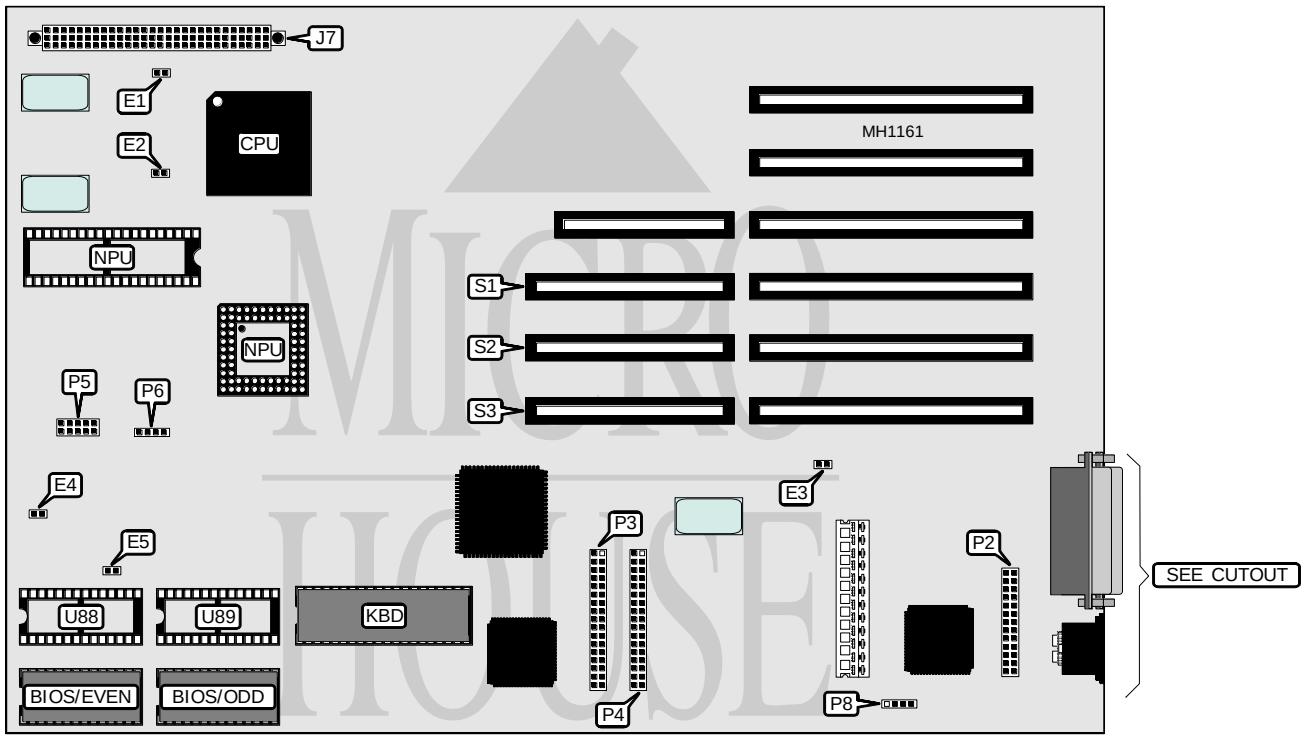
Processor	80386DX
Processor Speed	20MHz
Chip Set	AST
Max. Onboard DRAM	None (16MB on 32-bit external memory card)
Cache	None (64KB on 32-bit external memory card)
BIOS	AST
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot, parallel port, serial ports (2), SMART expansion/16-bit ISA expansion slots (3)
NPU Options	80287/80387



CONNECTIONS			
Purpose	Location	Purpose	Location
32-bit external memory card	J7	Front panel switches & LEDs	P5
SMART expansion or 16-bit ISA	S1, S2 & S3	Speaker	P6
Serial port 2 header	P2	External battery	P8
Floppy drive interface (A: and B:)	P3	Optional ROM (odd)	U89
Third floppy drive interface	P4	Optional ROM (even)	U88

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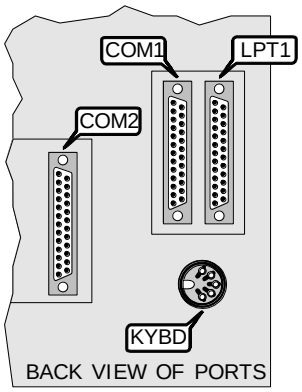

CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	COM1	Parallel port	LPT1
Serial port 2	COM2	Keyboard	KYBD

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	E1	closed
í Factory configured - do not alter	E2	closed
í Factory configured - do not alter	E3	closed
í Factory configured - do not alter	E4	open
í Factory configured - do not alter	E5	closed
í Factory configured - do not alter	E6	open
Note: Jumper E6 may not appear on all board revisions.		

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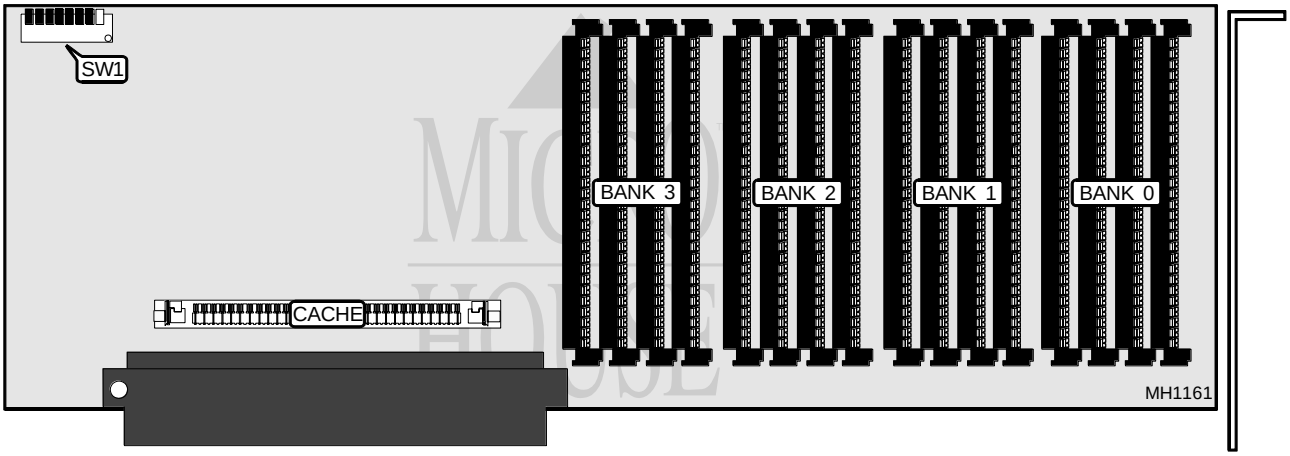

USER CONFIGURABLE SETTINGS		
Function	Switch	Position
í Parity check enabled	SW1/switch 7	closed
Parity check disabled	SW1/switch 7	open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB[1]	(4) 256K x 9	NONE	NONE	NONE
2MB[1]	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB[1]	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB[1]	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB[2]	(4) 1M x 9	NONE	NONE	NONE
5MB[1]	(4) 256K x 9	(4) 1M x 9	NONE	NONE
6MB[1]	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
7MB[1]	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9
8MB[2]	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB[1]	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
10MB[1]	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB[2]	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
13MB[1]	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB[2]	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
Notes [1] & [2] : Please see the next table for the proper switch settings for this configuration.				

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DRAM SWITCH CONFIGURATION								
Size	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
1MB[1]	open	open	open	open	open	open	closed	open
2MB[1]	open	closed	open	open	open	open	closed	open
3MB[1]	open	closed	open	closed	open	open	closed	open
4MB[1]	open	closed	open	closed	open	closed	closed	open
4MB[2]	open	open	open	closed	open	closed	closed	closed
5MB[1]	closed	open	open	open	open	open	closed	open
6MB[1]	open	closed	closed	open	open	open	closed	open
7MB[1]	open	closed	open	closed	closed	open	closed	open
8MB[2]	closed	open	open	open	open	open	closed	closed
9MB[1]	closed	open	closed	open	open	open	closed	open
10MB[1]	open	closed	closed	open	closed	open	closed	open
12MB[2]	closed	open	closed	open	open	open	closed	closed
13MB[1]	closed	open	closed	open	closed	open	closed	open
16MB[2]	closed	open	closed	open	closed	open	closed	closed
Notes [1] & [2] : Please see the previous table for the proper configuration for these switch settings.								

SRAM CONFIGURATION	
Size	Cache SRAM
64KB	(8) 8K x 8 on a SIMM-type module (AST Part# 202303-001)

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