## AST RESEARCH, INC.

PREMIUM / 386

| Processor | 80386 DX |
| :--- | :--- |
| Processor Speed | 20 MHz |
| Chip Set | AST |
| Max. Onboard DRAM | None |
| Cache | None |
| BIOS | AST |
| Dimensions | $330 \mathrm{~mm} \times 218 \mathrm{~mm}$ |
| I/O Options | 32 -bit external memory card slot |
|  | Parallel port |
|  | Serial port (2) |
|  | SMART expansion/16-bit ISA expansion slots (3) |
| NPU Options | 80287/80387 |



| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| 32-bit external memory card | J7 | Front panel switches \& LEDs | P5 |
| SMART expansion or 16-bit ISA | S1, S2 \& S3 | Speaker | P6 |
| Serial port 2 header | P2 | External battery | P8 |
| Floppy drive interface (A: and B:) | P3 | Optional ROM (even) | U88 |
| Third floppy drive interface | P4 | Optional ROM (odd) | U89 |

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| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| Serial port 1 | COM1 | Parallel port | LPT1 |
| Serial port 2 | COM2 | Keyboard | KYBD |

## USER CONFIGURABLE SETTINGS

| Function |  | Jumper |
| :--- | :---: | :---: |
| í Factory configured - do not alter | E1 | closed |
| í Factory configured - do not alter | E2 | closed |
| í Factory configured - do not alter | E3 | closed |
| í Factory configured - do not alter | E4 | open |
| í Factory configured - do not alter | E5 | closed |
| í Factory configured - do not alter | E6 | open |
| Note: Jumper E6 may not appear on all board revisions. |  |  |

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| USER CONFIGURABLE SETTINGS |  |  |  |
| :--- | :---: | :---: | :---: |
| Function |  |  |  |
| í Parity check enabled SW1 | Position |  |  |
| Parity check disabled | switch 7 | closed |  |
| í Factory configured - do not alter | switch 7 | open |  |


| DRAM CONFIGURATION |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 1MB | (4) $256 \mathrm{~K} \times 9$ | NONE | NONE | NONE |
| 2MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | NONE | NONE |
| 3 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | NONE |
| 4MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ |
| 5 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $1 \mathrm{M} \times 9$ | NONE | NONE |
| 6 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | (4) $1 \mathrm{M} \times 9$ | NONE |
| 7MB | (4) 256K x 9 | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | (4) 1 M x 9 |
| 9 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ | NONE |
| 10MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ |
| 13MB | (4) $256 \mathrm{~K} \times 9$ | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ |
| Bank 0 must be populated with 256 KB SIMMs. |  |  |  |  |


| DRAM SWITCH CONFIGURATION |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Size | SW1/switch 1 |  |  |  |  |  |  | SW1/switch 2 | SW1/switch 3 | SW1/switch 4 | SW1/switch 5 |  | SW1/switch 6 |
| 1 MB | open | open | open | open | open | open |  |  |  |  |  |  |  |
| 2 MB | open | closed | open | open | open | open |  |  |  |  |  |  |  |
| 3 MB | open | closed | open | closed | open | open |  |  |  |  |  |  |  |
| 4 MB | open | closed | open | closed | open | closed |  |  |  |  |  |  |  |
| 5 MB | closed | open | open | open | open | open |  |  |  |  |  |  |  |
| 6 MB | open | closed | closed | open | open | open |  |  |  |  |  |  |  |
| 7 MB | open | closed | open | closed | closed | open |  |  |  |  |  |  |  |
| 9 MB | closed | open | closed | open | open | open |  |  |  |  |  |  |  |
| 10 MB | open | closed | closed | open | closed | open |  |  |  |  |  |  |  |
| 13 MB | closed | open | closed | open | open | open |  |  |  |  |  |  |  |

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