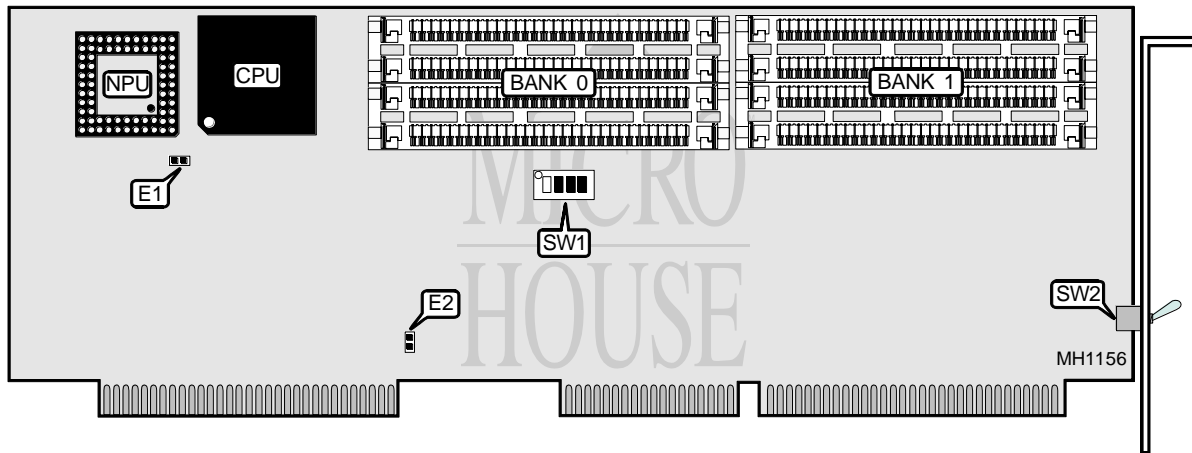


# AST RESEARCH, INC.

## FASTBOARD/386 FOR THE PREMIUM/286

### MODELS 202143-00X

<b>Processor</b>	80286/80386DX
<b>Processor Speed</b>	10MHz
<b>Chip Set</b>	AST
<b>Max. Onboard DRAM</b>	8MB
<b>SRAM Cache</b>	None
<b>BIOS</b>	AST
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387



USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU disabled	E1	open
NPU enabled	E1	closed
í Factory configured - do not alter	E2	open
í Parity check enabled	SW1/switch 4	open
Parity check disabled	SW1/switch 4	closed

Note: SW2 is used to toggle between the 80286 and the 80386 CPUs at power-on.

DRAM CONFIGURATION					
Size	SW1/switch 1	SW1/switch 2	SW1/switch 3	Bank 0	Bank 1
1MB	open	open	open	(4) 256K x 9	NONE
2MB	closed	open	open	(4) 256K x 9	(4) 256K x 9
4MB	open	closed	open	(4) 1M x 9	NONE
5MB	closed	open	closed	(4) 256K x 9	(4) 1M x 9
8MB	closed	closed	closed	(4) 1M x 9	(4) 1M x 9

Note: When using the FASTboard/386 in a Premium/286 with a FASTram Memory Board installed, SW1/switch 5 should be in the open position and SW1/switch 6 to SW1/switch 10 should be left in their default positions on the FASTram Memory Board.