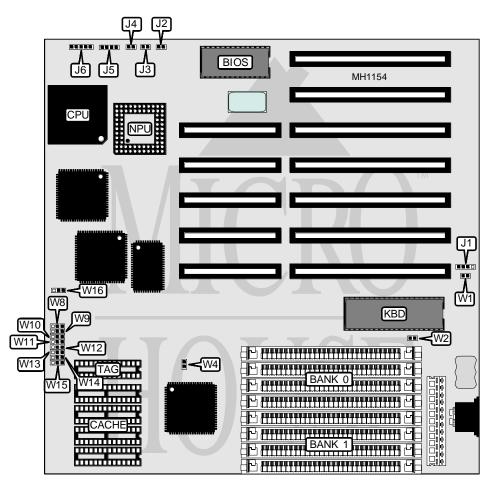
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U 3 8 6

Processor	80386
Processor Speed	33/401
Chip Set	OPTI
Max. Onboard DRAM	32MB
SRAM Cache	32/128
BIOS	AMI
Dimensions	220mr
I/O Options	NONE
NPU Options	80387

80386DX 33/40MHz OPTI 32MB 32/128KB AMI 220mm x 220mm NONE 80387



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Reset switch	J4
Turbo LED	J2	Speaker	J5
Turbo switch	J3	Power LED & keylock	J6

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	W1	closed
CMOS memory clear	W1	open
í Monitor type select color	W2	closed
Monitor type select monochrome	W2	open
í NPU disabled	W4	open
NPU enabled	W4	closed
í Parity check enabled	W16	pins 2 & 3 closed
Parity check disabled	W16	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

SRAM JUMPER CONFIGURATION		
Jumper	32KB	128KB
W8	pins 1 & 2 closed	pins 2 & 3 closed
W9	pins 1 & 2 closed	pins 2 & 3 closed
W10	pins 1 & 2 closed	pins 2 & 3 closed
W11	pins 1 & 2 closed	pins 2 & 3 closed
W12	pins 2 & 3 closed	pins 2 & 3 closed
W13	pins 1 & 2 closed	pins 1 & 2 closed
W14	pins 1 & 2 closed	pins 1 & 2 closed
W15	open	pins 1 & 2 closed

SRAM CONFIGURATION		
Size	Cache SRAM	TAG
32KB	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	(1) 8K x 8