ANIX TECHNOLOGY CORPORATION
64KB CACHE 386 25/33MHZ

Processor	80386SX
Processor Speed	25/33MHz
Chip Set	None
Max. Onboard DRAM	16MB
Cache	64/256KB
BIOS	AMI
Dimensions	355mm x 304mm
I/O Options	None
NPU Options	80387/3167
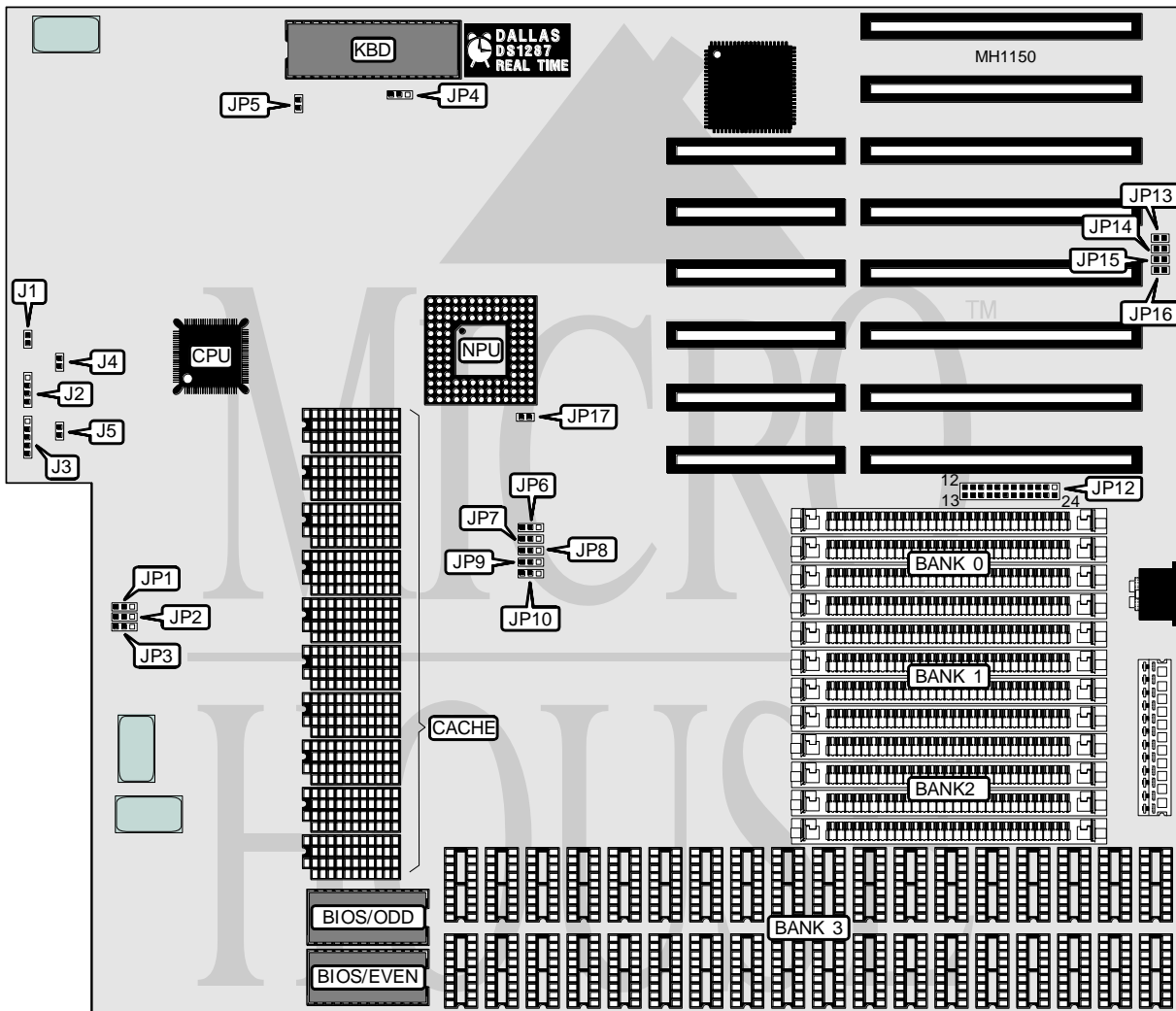
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J1	Turbo LED	J4
Speaker	J2	Reset switch	J5
Power LED & keylock	J3		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í I/O bus (8-bit) wait states select seven	JP1	pins 1 & 2 closed
I/O bus (8-bit) wait states select five	JP1	pins 2 & 3 closed
í I/O bus (16-bit) wait states select five	JP2	pins 2 & 3 closed
I/O bus (16-bit) wait states select three	JP2	pins 1 & 2 closed
í I/O bus speed select CPU/4	JP3	pins 2 & 3 closed
I/O bus speed select CPU/2.5	JP3	pins 1 & 2 closed
í Monitor type select monochrome	JP4	pins 2 & 3 closed
Monitor type select color	JP4	pins 1 & 2 closed
í NPU disabled	JP5	Open
NPU enabled	JP5	Closed
í NPU mode select synchronous with CPU (80387)	JP17	Open
NPU mode select asynchronous with CPU (80387)	JP17	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB[1]	(4) 256K x 9	NONE	NONE	NONE
2MB[1]	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB[1]	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB[1]	(4) 1M x 9	NONE	NONE	NONE
4MB[2]	NONE	NONE	NONE	(36) 411000
5MB[1]	(4) 256K x 9	(4) 1M x 9	NONE	NONE
7MB[1]	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(36) 411000
8MB[1]	(4) 1M x 9	(4) 1M x 9	NONE	NONE
8MB[2]	NONE	(4) 1M x 9	NONE	(36) 411000
9MB[1]	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	NONE
9MB[2]	NONE	(4) 1M x 9	(4) 256K x 9	(36) 411000
10MB[2]	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	(36) 411000
12MB[1]	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
12MB[2]	NONE	(4) 1M x 9	(4) 1M x 9	(36) 411000
16MB[1]	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(36) 411000
Note[1&2]: See the next table for settings.				

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DRAM JUMPER CONFIGURATION							
Size	JP8	JP9	JP10	JP13	JP14	JP15	JP16
1MB[1]	2 & 3	N/A	N/A	Closed	Closed	Closed	Closed
2MB[1]	2 & 3	2 & 3	N/A	Closed	Closed	Closed	Open
3MB[1]	2 & 3	2 & 3	2 & 3	Closed	Closed	Open	Closed
4MB[1]	1 & 2	N/A	N/A	Closed	Closed	Open	Open
4MB[2]	N/A	N/A	N/A	Closed	Closed	Open	Open
5MB[1]	2 & 3	1 & 2	N/A	Closed	Open	Closed	Closed
7MB[1]	2 & 3	2 & 3	2 & 3	Closed	Open	Closed	Open
8MB[1]	1 & 2	1 & 2	N/A	Closed	Open	Open	Closed
8MB[2]	N/A	1 & 2	N/A	Closed	Open	Open	Closed
9MB[1]	1 & 2	1 & 2	2 & 3	Closed	Open	Open	Open
9MB[2]	N/A	1 & 2	2 & 3	Closed	Open	Open	Open
10MB[2]	2 & 3	1 & 2	2 & 3	Open	Closed	Closed	Closed
12MB[1]	1 & 2	1 & 2	1 & 2	Open	Closed	Closed	Open
12MB[2]	N/A	1 & 2	1 & 2	Open	Closed	Closed	Open
16MB[1]	1 & 2	1 & 2	1 & 2	Open	Closed	Open	Closed
Note[1]: Set JP12 pins 1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10, 11 & 12, 13 & 14, 15 & 16, closed.							
Note[2]: Set JP12 pins 1 & 24, 2 & 23, 3 & 22, 4 & 21, 5 & 20, 6 & 19, 7 & 18, 8 & 17, closed.							

CACHE CONFIGURATION	
Size	Bank 0
64KB	(10) 16K x 4
256KB	(10) 64K x 4

CACHE JUMPER CONFIGURATION		
Size	JP6	JP7
64KB	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed