## UNIDENTIFIED

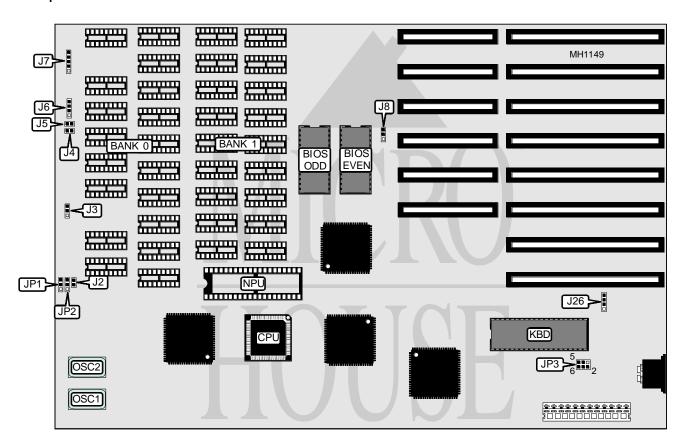
## BIAT-12/BIAT-16/BIAT-20/BIAT-24

80286 **Processor** 

**Processor Speed** 12/16/20/24MHz C & T NEAT **Chip Set** Max. Onboard DRAM 4MB Cache None **BIOS** Phoenix

**Dimensions** 330mm x 218mm

I/O Options None **NPU Options** 80287



CONNECTIONS				
Purpose	Location	Purpose	Location	
Turbo switch	J2	Speaker	J6	
Turbo LED	J3	Power LED & keylock	J7	
Reset switch	J4	External battery	J26	

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## BIAT-12/BIAT-16/BIAT-20/BIAT-24

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Monitor type select color	J5	Closed		
Monitor type select monochrome	J5	Open		
í BIOS type select 27256	J8	pins 1 & 2 closed		
BIOS type select 27128	J8	pins 2 & 3 closed		
í I/O bus select 0 wait states	JP1	pins 1 & 2 closed		
I/O bus select 1 wait state	JP1	pins 2 & 3 closed		
í NPU mode select synchronous with CPU	JP2	pins 1 & 2 closed		
NPU mode select synchronous with oscillator at OSC1	JP2	pins 2 & 3 closed		
í Keyboard controller set on pin 23	JP3	pins 1 & 2 closed		
Keyboard controller set on pin 27	JP3	pins 3 & 4 closed		
Keyboard controller set on pin 30	JP3	pins 5 & 6 closed		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
512KB	(18) 41256	NONE		
640KB	(18) 41259	(18) 4164		
1MB	(18) 41256	(18) 41256		
2MB	(18) 411000	NONE		
4MB	(18) 411000	(18) 411000		