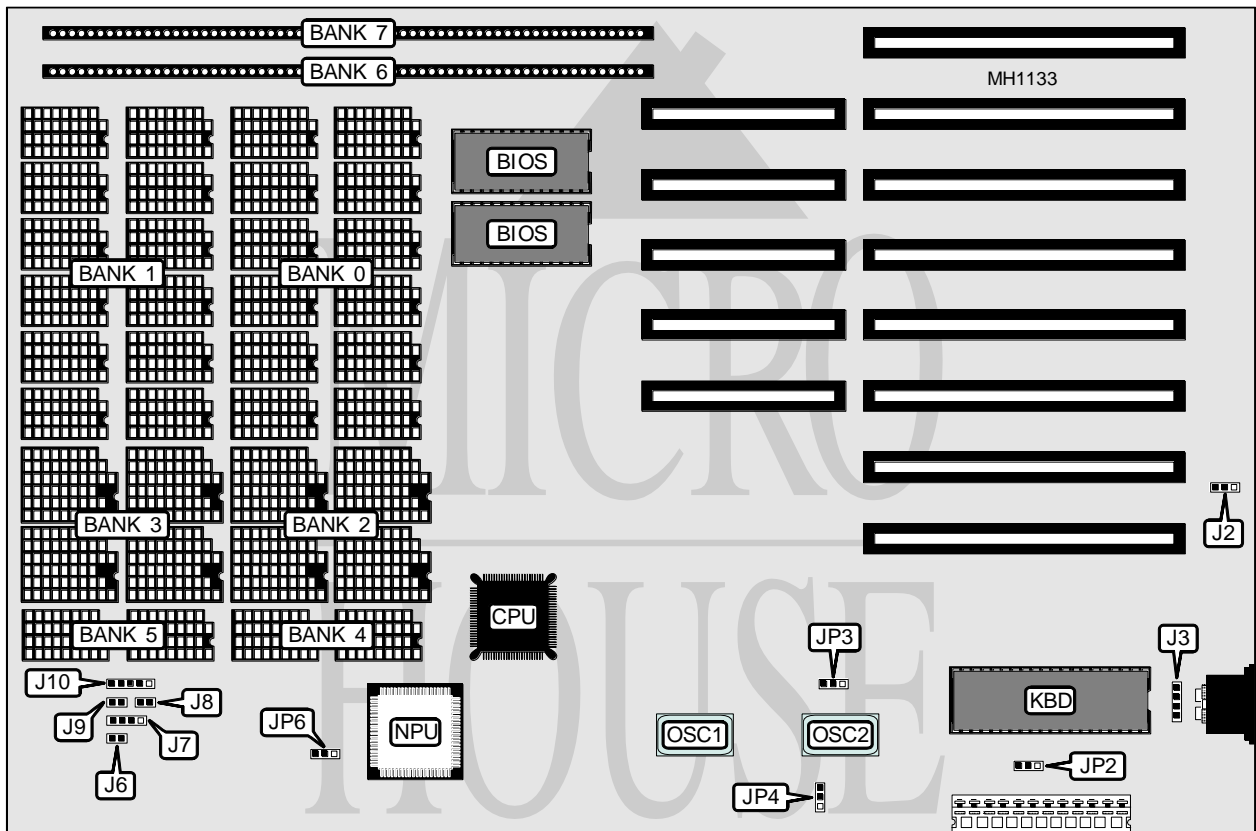


JAMECO ELECTRONIC COMPONENTS

JE3516SN

Processor	80386SX
Processor Speed	16MHz
Chip Set	C & T NEAT
Max. Onboard DRAM	8MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Turbo LED	J8
Turbo switch	J6	Reset switch	J9
Speaker	J7	Power LED & keylock	J10

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JAMECO ELECTRONIC COMPONENTS

JE3516SN

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	J2	pins 1 & 2 closed
Battery type select external	J2	pins 2 & 3 closed
í Monitor type select monochrome	JP1	pins 1 & 2 closed
Monitor type select color	JP1	pins 2 & 3 closed
í CPU speed select OSC1/2	JP2	pins 1 & 2 closed
CPU speed select OSC2/2	JP2	pins 2 & 3 closed
í Pipeline mode enabled	JP3	pins 1 & 2 closed
Pipeline mode disabled	JP3	pins 2 & 3 closed
í Power supply type select AT	JP4	pins 1 & 2 closed
Power supply type select XT	JP4	pins 2 & 3 closed
Note: Jumper JP1 may not be present on all board revisions.		

DRAM CONFIGURATION									
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5	Bank 6	Bank 7	Intrlive
640KB ¹	41256	4164	41256	4164	41256	4164	NONE	NONE	NONE
640KB ¹	NONE	4164	514256	NONE	41256	NONE	NONE	NONE	NONE
1MB ¹	41256	41256	41256	41256	41256	41256	NONE	NONE	2-Way
1MB ¹	NONE	NONE	514256	514256	41256	41256	NONE	NONE	2-Way
1MB ²	NONE	NONE	NONE	NONE	NONE	NONE	256K x 9	256K x 9	2-Way
1.5MB ¹	41256	41256	41256	41256	41256	41256	256K x 9	NONE	2-Way
1.5MB ¹	NONE	NONE	514256	514256	41256	41256	256K x 9	NONE	2-Way
2MB ¹	41256	41256	41256	41256	41256	41256	256K x 9	NONE	4-Way
2MB ¹	NONE	NONE	514256	514256	41256	41256	256K x 9	256K x 9	4-Way
2MB ¹	411000	NONE	411000	NONE	411000	NONE	NONE	NONE	NONE
2MB ²	NONE	NONE	NONE	NONE	NONE	NONE	1M x 9	NONE	NONE
3MB ¹	41256	41256	41256	41256	41256	41256	1M x 9	NONE	2-Way
3MB ¹	NONE	NONE	514256	514256	41256	41256	1M x 9	NONE	2-Way
4MB ¹	411000	411000	411000	411000	411000	411000	NONE	NONE	2-Way
4MB ²	NONE	NONE	NONE	NONE	NONE	NONE	1M x 9	1M x 9	2-Way
5MB ¹	41256	41256	41256	41256	41256	41256	1M x 9	1M x 9	2-Way
5MB ¹	NONE	NONE	514256	514256	41256	41256	1M x 9	1M x 9	2-Way
6MB ¹	411000	411000	411000	411000	411000	411000	1M x 9	NONE	4-Way
8MB ¹	411000	411000	411000	411000	411000	411000	1M x 9	1M x 9	4-Way
Notes: Bank 0 and Bank 1 use 12 of the specified DIPP DRAM per bank. Bank 2 and Bank 3 use 4 of the specified DIPP DRAM per bank. Bank 4 and Bank 5 use 2 of the specified DIPP DRAM per bank. Bank 6 and Bank 7 use 2 of the specified SIPP DRAM per bank. Memory interleaving (Intrlive) is automatically configured and is not alterable.									
Note ¹ : On Jumper JP6, pins 1 & 2 should be in the closed position.									
Note ² : On Jumper JP6, pins 2 & 3 should be in the closed position.									