YOUNG MICRO SYTEMS, INC.
V E G A   4 8 6 F - V L

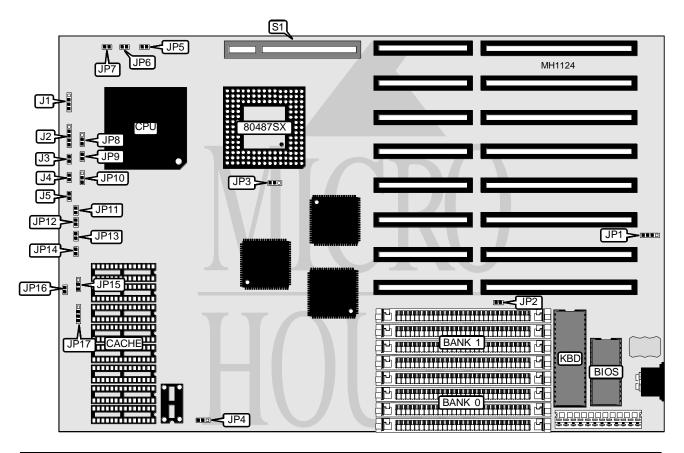|	
Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	16/20/25/33/50(internal)/50/66(internal)MHz
Chip Set	Vega
Max. Onboard DRAM	32MB
Cache	64/256/1024KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slot
NPU Options	None

CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Turbo switch	J5
Power LED & keylock	J2	External battery	JP1
Reset switch	J3	32-bit VESA local bus slot	S1
Turbo LED	J4		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í CMOS memory normal operation	JP3	pins 1 & 2 closed
CMOS memory clear	JP3	pins 2 & 3 closed
í Factory configured - do not alter	JP4	pins 1 & 2 closed
í I/O wait states select zero	JP5	Open
I/O wait states select one	JP5	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION	
Size	Bank 0
64KB	(8) 8K x 8
256KB	(8) 32K x 8
1MB	(8) 128K x 8

CACHE JUMPER CONFIGURATION							
Size	JP11	JP12	JP13	JP14	JP15	JP16	JP17
64KB	Open	Open	Open	Open	1 & 2	Closed	1 & 2, 2 & 3
256KB	Open	Open	Closed	Closed	2 & 3	Open	1 & 2, 2 & 3
1MB	Closed	Closed	Closed	Closed	2 & 3	Open	2 & 3
Note: Pins designated should be in the closed position.							

CPU TYPE CONFIGURATION			
Type	JP8	JP9	JP10
80486SX	Open	Open	pins 2 & 3 closed
ODP486SX	Open	Open	pins 2 & 3 closed
80487SX	pins 2 & 3 closed	Closed	pins 1 & 2 closed
80486DX	pins 1 & 2 closed	Closed	pins 1 & 2 closed
80486DX2	pins 1 & 2 closed	Closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION		
Speed	JP6	JP7
16/20/25/33MHz	open	closed
40MHz	closed	open
50MHz	closed	closed