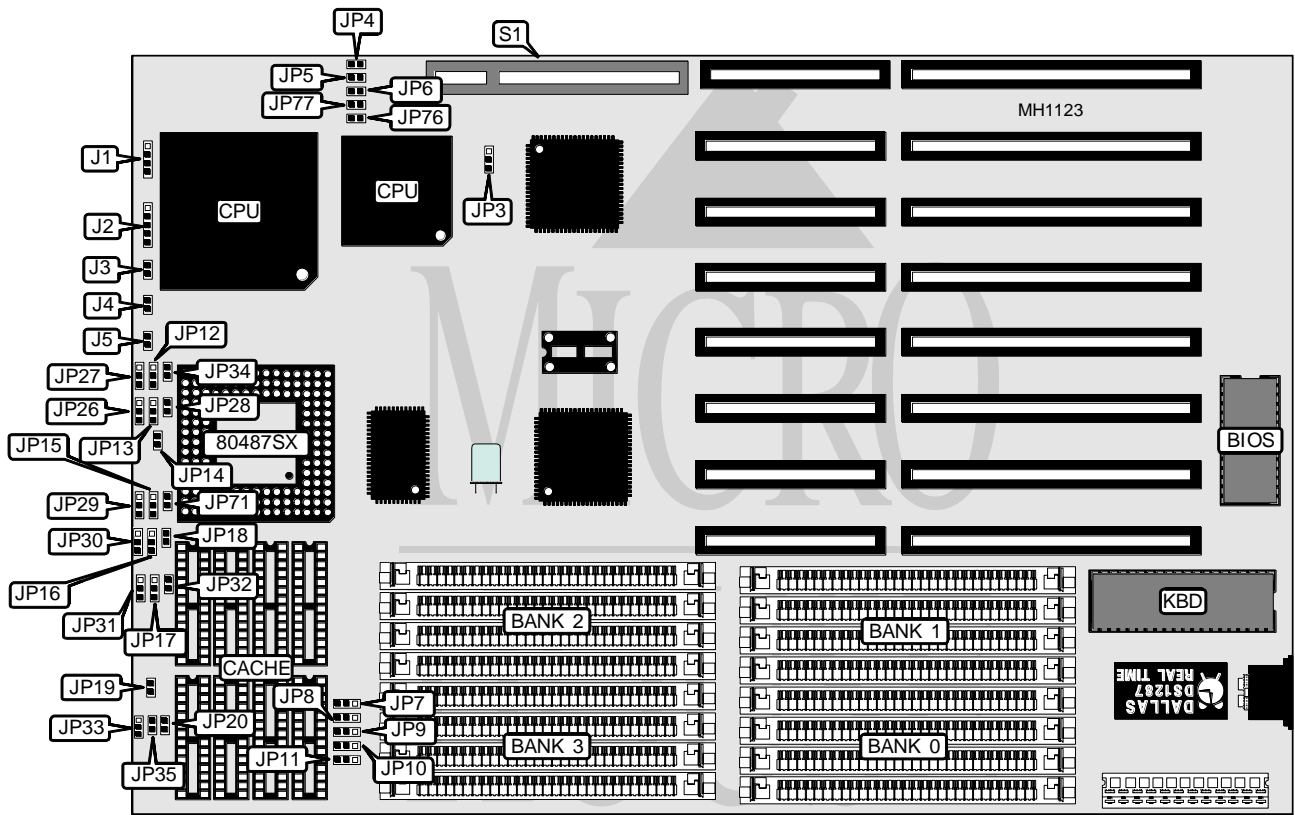


YOUNG MICRO SYTEMS, INC.

VEGA 3486S-VL

Processor	80386DX/80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	16/20/25/33/50(internal)/50/66(internal)MHz
Chip Set	Symphony
Max. Onboard DRAM	64MB
Cache	64/256/1024KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slot
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Turbo LED	J4
Power LED & keylock	J2	Turbo switch	J5
Reset	J3	32-bit VESA local bus slot	S1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP3	pins 1 & 2 closed
í Factory configured - do not alter	JP12	pins 1 & 2 closed
í Factory configured - do not alter	JP27	pins 1 & 2 closed
í Factory configured - do not alter	JP34	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
5MB	(4) 1M x 9	(4) 256K x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
7MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
13MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
52MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION	
Size	Bank 0
64KB	(8) 8K x 8
256KB	(8) 32K x 8
1MB	(8) 128K x 8

CACHE JUMPER CONFIGURATION					
Size	JP7	JP8	JP9	JP10	JP11
64KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2
1MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position

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CPU TYPE CONFIGURATION					
Type	JP13	JP14	JP15	JP16	JP17
80386DX	Open	Closed	1 & 2	1 & 2	1 & 2
80486SX	1 & 2	Open	2 & 3	2 & 3	2 & 3
80487SX	2 & 3	Open	2 & 3	2 & 3	2 & 3
80486DX	1 & 2	Open	2 & 3	2 & 3	2 & 3
80486DX-50	1 & 2	Open	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position

CPU TYPE CONFIGURATION (CON'T)					
Type	JP18	JP19	JP26	JP28	JP29
80386DX	Closed	Open	Open	Open	1 & 2
80486SX	Open	Open	Open	Open	2 & 3
80487SX	Open	Open	2 & 3	Closed	2 & 3
80486DX	Open	Open	1 & 2	Closed	2 & 3
80486DX-50	Open	Closed	1 & 2	Closed	2 & 3

Note: Pins designated should be in the closed position

CPU TYPE CONFIGURATION (CON'T)						
Type	JP30	JP31	JP32	JP71	JP76	JP77
80386DX	1 & 2	1 & 2	Closed	Closed	Open	Closed
80486SX	2 & 3	2 & 3	Open	Open	Closed	Open
80487SX	2 & 3	2 & 3	Open	Open	Closed	Open
80486DX	2 & 3	2 & 3	Open	Open	Closed	Open
80486DX-50	2 & 3	2 & 3	Open	Open	Closed	Open

Note: Pins designated should be in the closed position

CPU SPEED CONFIGURATION			
Speed	JP4	JP5	JP33
16/20/25MHz	Open	Closed	Open
33MHz	Open	Closed	pins 1 & 2 closed
33MHz	Closed	Open	pins 2 & 3 closed
50/66MHz	Open	Open	pins 2 & 3 closed

I/O WAIT STATE CONFIGURATION			
Wait states	JP6	JP20	JP35
0	Closed	Open	Closed
1	Open	Closed	Open