JOINDATA SYSTEMS, INC.
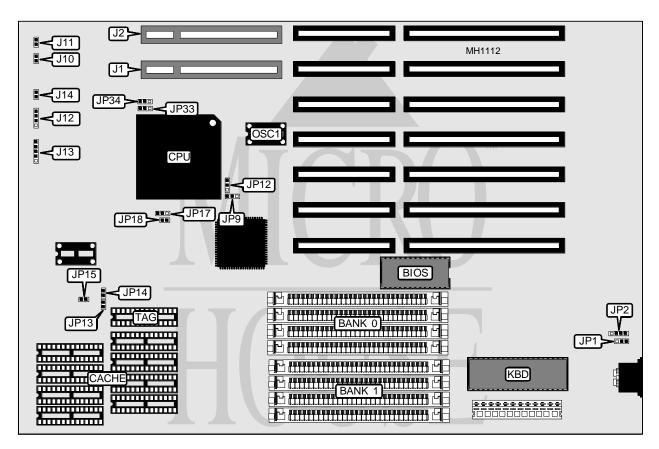# G 4 8 6 E V L

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	16/20/25/33/50(internal)/50/66(internal)MHz
Chip Set	EFAR
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None

CONNECTIONS			
Purpose	Location	Purpose	Location
32-bit VESA local bus slots	J1 & J2	Power LED & keylock	J13
Turbo LED	J10	Reset switch	J14
Turbo switch	J11	External battery	JP2
Speaker	J12		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í CPU speed select 20/25/33MHz	JP33	pins 2 & 3 closed
CPU speed select 50MHz	JP33	pins 1 & 2 closed
í VESA bus select 0 wait states	JP34	pins 1 & 2 closed
VESA bus select 1 wait state	JP34	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
64KB	(8) 8K x 8	(1) 8K x 8
256KB	(8) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP13	JP14	JP15
64KB	Open	Open	Open
256KB	Closed	Closed	Closed

CPU TYPE CONFIGURATION					
Type	OSC1	JP9	JP12	JP17	JP18
80486SX-20	20MHz	2 & 3	Open	2 & 3	Open
80486SX-20	40MHz	1 & 2	Open	2 & 3	Open
80486SX-25	25MHz	2 & 3	Open	2 & 3	Open
80486SX-25	50MHz	1 & 2	Open	2 & 3	Open
80486DX-25	50MHz	1 & 2	2 & 3	1 & 2	Closed
80486DX-25	25MHz	2 & 3	2 & 3	1 & 2	Closed
80487SX-25	50MHz	1 & 2	1 & 2	1 & 2	Closed
80487SX-25	25MHz	2 & 3	1 & 2	1 & 2	Closed
80486DX-33	66MHz	1 & 2	2 & 3	1 & 2	Closed
80486DX-33	33MHz	2 & 3	2 & 3	1 & 2	Closed
80486DX2-50	25MHz	2 & 3	2 & 3	1 & 2	Closed
80486DX2-66	33MHz	2 & 3	2 & 3	1 & 2	Closed
Note: Pins designated should be in the closed position.					