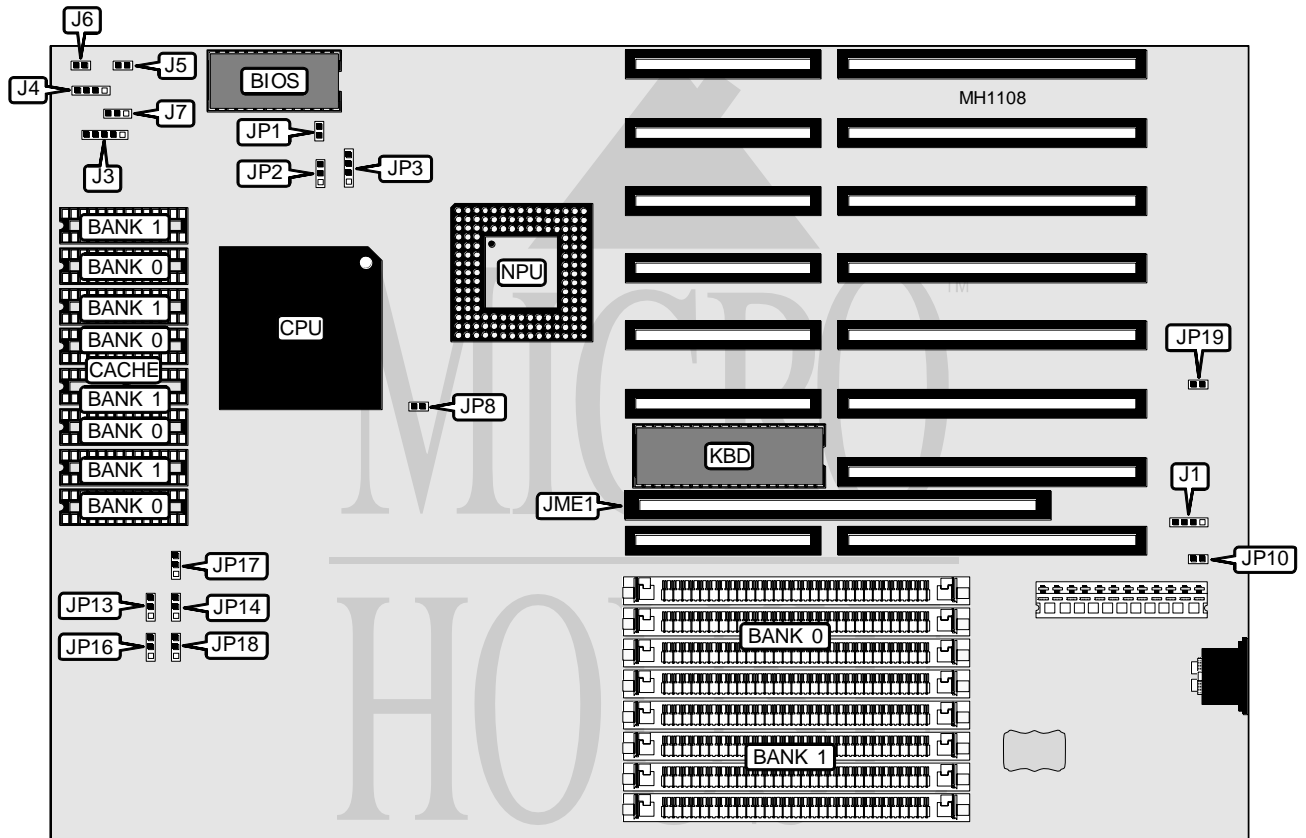


ADVANCED INTEGRATION RESEARCH, INC.

486US

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/50MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI/Phoenix
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J6
Power LED & keylock	J3	Turbo LED	J7
Speaker	J4	32-bit external memory card	JME1
Reset switch	J5		

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486US

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP10	Closed
Monitor type select monochrome	JP10	Open
í CMOS memory normal operation	JP19	pins 1 & 2 closed
CMOS memory clear	JP19	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	JP13	JP14	JP16	JP17	JP18
64KB	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
128KB	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
256KB	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION				
Type	JP1	JP2	JP3	JP8
80486SX	Open	pins 2 & 3 closed	pins 3 & 4 closed	Open
80487SX	Closed	pins 1 & 2 closed	pins 2 & 3 closed	Open
80486DX	Closed	pins 1 & 2 closed	pins 1 & 2 closed	Open