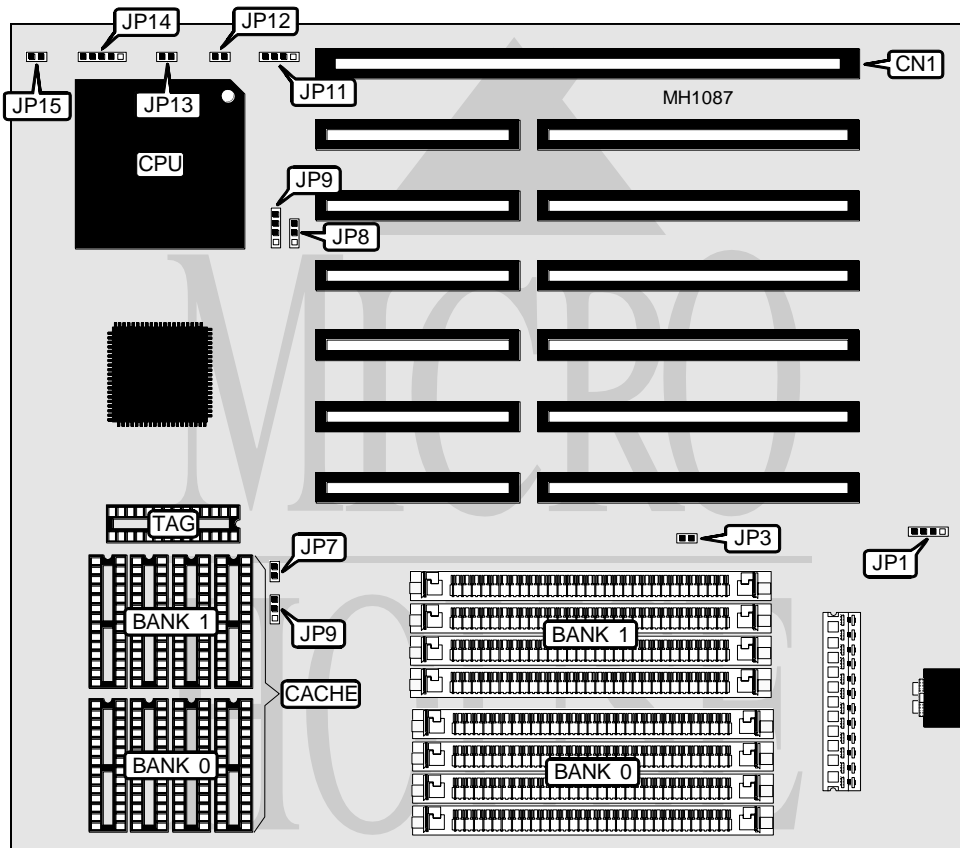


UNIDENTIFIED 80486L-ISA

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI/Award
Dimensions	254mm x 218mm
I/O Options	32-bit local bus card slot
NPU Options	None



CONNECTIONS

Purpose	Location	Purpose	Location
32-bit local bus card	CN1	Reset switch	JP13
External battery	JP1	Power LED & keylock	JP14
Speaker	JP11	Turbo LED	JP15
Turbo switch	JP12		

Continued on next page . . .

UNIDENTIFIED

80486L-ISA

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP3	Open
Monitor type select monochrome	JP3	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP6	JP7
64KB	Open	Open
128KB	pins 1 & 2 closed	Closed
256KB	pins 2 & 3 closed	Closed

CPU TYPE CONFIGURATION					
Type	Speed	Oscillator	JP4	JP8	JP9
80486SX	16MHz	32MHz	1 & 2	Open	2 & 3
80486SX	20MHz	40MHz	1 & 2	Open	2 & 3
ODP486SX	50MHz	25MHz	2 & 3	1 & 2	1 & 2, 3 & 4
80487SX	25MHz	25MHz	2 & 3	1 & 2	1 & 2, 3 & 4
80486DX	25MHz	25MHz	2 & 3	2 & 3	1 & 2, 3 & 4
80486DX	33MHz	33MHz	2 & 3	2 & 3	1 & 2, 3 & 4
80486DX	50MHz	50MHz	2 & 3	2 & 3	1 & 2, 3 & 4
80486DX2	50MHz	25MHz	2 & 3	2 & 3	1 & 2, 3 & 4
80486DX2	66MHz	33MHz	2 & 3	2 & 3	1 & 2, 3 & 4

Note: The location of JP4 is unidentified. Pins designated should be in the closed position.