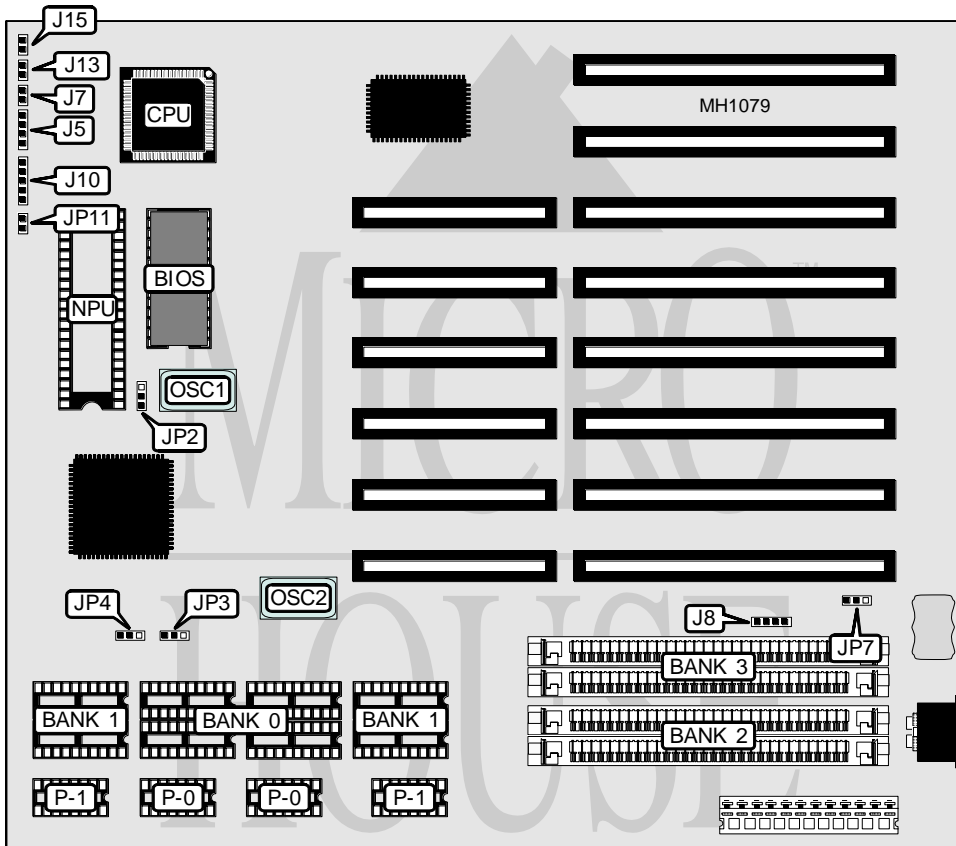


# J-MARK COMPUTER CORPORATION

## JET-286V

<b>Processor</b>	80286
<b>Processor Speed</b>	16/20MHz
<b>Chip Set</b>	VLSI SCAMP
<b>Max. Onboard DRAM</b>	16MB
<b>Cache</b>	None
<b>BIOS</b>	Award
<b>Dimensions</b>	254mm x 220mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80287



### CONNECTIONS

Purpose	Location	Purpose	Location
Speaker	J5	Power LED & keylock	J10
External battery	J8	Turbo LED	J13
Turbo switch	J7	Reset switch	J15

*Continued on next page . . .*

# J-MARK COMPUTER CORPORATION

## JET-286V

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU mode select synchronous with CPU	JP2	pins 1 & 2 closed
NPU mode select synchronous with oscillator installed at OSC1	JP2	pins 2 & 3 closed
í Battery type select internal	JP7	pins 2 & 3 closed
Battery type select external	JP7	pins 1 & 2 closed
í Monitor type select color	JP11	Closed
Monitor type select monochrome	JP11	Open

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(1) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(1) 1M x 9	(1) 1M x 9

DRAM JUMPER CONFIGURATION		
Size	JP3	JP4
512KB	pins 2 & 3 closed	pins 2 & 3 closed
512KB	pins 1 & 2 closed	pins 1 & 2 closed
1MB	pins 2 & 3 closed	pins 2 & 3 closed
1MB	pins 1 & 2 closed	pins 1 & 2 closed
2MB	pins 2 & 3 closed	pins 2 & 3 closed
2MB	pins 1 & 2 closed	pins 1 & 2 closed
4MB	pins 1 & 2 closed	pins 1 & 2 closed