## GIGA-BYTE TECHNOLOGY CO., LTD

GA-386UM 33/40 MAIN BOARD

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
SRAM Cache
BIOS
Dimensions
I/O Options
NPU Options

80386DX
33/40MHz
OPTI
32MB
64/128/256KB
AMI
$220 \mathrm{~mm} \times 260 \mathrm{~mm}$
None
80387/3167


| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| Power LED \& keylock | J | Turbo switch | J 4 |
| Speaker | Turbo LED | J 5 |  |
| Reset switch | J3 | External battery | J 11 |
| Note: Board may be supplied with either a PGA (socketed) or PQFP (surface mounted) CPU. |  |  |  |

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| USER CONFIGURABLE SETTINGS |  |  |
| :--- | :---: | :---: |
| Function | Jumper |  |
| í NPU absent | J 7 | open |
| NPU present | J 7 | closed |
| í Monitor type select color | J 8 | closed |
| Monitor type select monochrome | J 8 | open |
| í CMOS memory normal operation | J 9 | pins 2 \& 3 closed |
| CMOS memory clear | J 9 | pins 1 \& 2 closed |


| DRAM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 |
| 1 MB | (4) $256 \mathrm{~K} \times 9$ | NONE |
| 2 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ |
| 4 MB | (4) $1 \mathrm{M} \times 9$ | NONE |
| 8 MB | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ |
| 16 MB | (4) $4 \mathrm{M} \times 9$ | NONE |
| 32 MB | (4) $4 \mathrm{M} \times 9$ | (4) $4 \mathrm{M} \times 9$ |


| SRAM CONFIGURATION |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Size | Cache SRAM | Location | TAG |  |
| 64 KB | (8) $8 \mathrm{~K} \times 8$ | Banks 0 \& 1 | $(1) 8 \mathrm{~K} \times 8$ |  |
| 128 KB | (4) $32 \mathrm{~K} \times 8$ | Bank 0 | $(1) 8 \mathrm{~K} \times 8$ |  |
| 256 KB | (8) $32 \mathrm{~K} \times 8$ | Banks $0 \& 1$ | (1) $32 \mathrm{~K} \times 8$ |  |


| SRAM JUMPER CONFIGURATION |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Size | WC1 | WC2 | WC3 | WC4 | WC5 |
| 64 KB | pins 2 \& 3 closed | open | open | pins $1 \& 2$ closed | pins $1 \& 2$ closed |
| 128 KB | pins $1 \& 2$ closed | open | closed | pins $2 \& 3$ closed | pins $1 \& 2$ closed |
| 256 KB | pins 2 \& 3 closed | closed | closed | pins 2 \& closed | pins 2 \& 3 closed |

