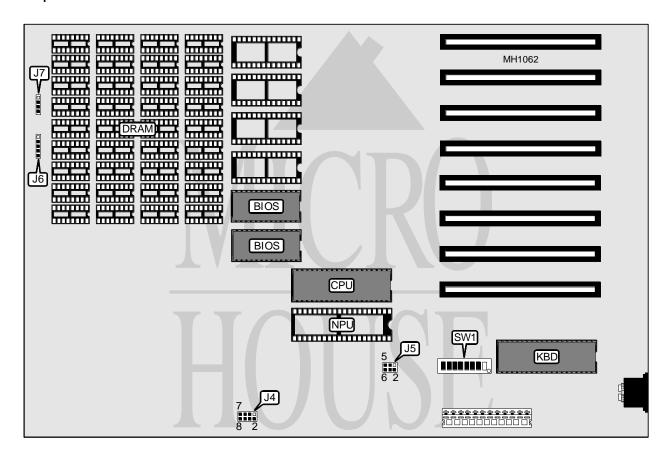
AMERICAN RESEARCH CORPORATION MODEL 8/10 SYSTEM

8088 **Processor Processor Speed** 8/10MHz **Chip Set** None Max. Onboard DRAM 640KB Cache None **BIOS** ARC

330mm x 218mm **Dimensions**

I/O Options None **NPU Options** 8087



CONNECTIONS				
Purpose	Location	Purpose	Location	
Power LED & keylock	J6	Speaker	J7	

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AMERICAN RESEARCH CORPORATION MODEL 8/10 SYSTEM

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USER CONFIGURABLE SETTINGS				
Function	Jumper/Switch	Position		
í Wait states select zero	J4	pins 7 & 8 closed		
Wait state select one	J4	pins 5 & 6 closed		
Wait states select two	J4	pins 3 & 4 closed		
Wait states select three	J4	pins 1 & 2 closed		
í CPU speed select switchable from keyboard	J5	pins 5 & 6 closed		
CPU speed select fast	J5	pins 3 & 4 closed		
CPU speed select slow	J5	pins 1 & 2 closed		
í DRAM test at power-on enabled	SW1/1	Off		
DRAM test at power-on disabled	SW1/1	On		
í NPU disabled	SW1/2	On		
NPU enabled	SW1/2	Off		
í Factory configured - do not alter	SW1/3	Off		
í Factory configured - do not alter	SW1/4	Off		

FLOPPY DRIVE CONFIGURATION					
Drives installed	SW1/7	SW1/8			
1	On	On			
2	Off	On			
3	On	Off			
4	Off	Off			

MONITOR CONFIGURATION					
Туре	SW1/5	SW1/6			
Monochrome	Off	Off			
Color (80 X 25)	On	Off			
EGA	On	On			