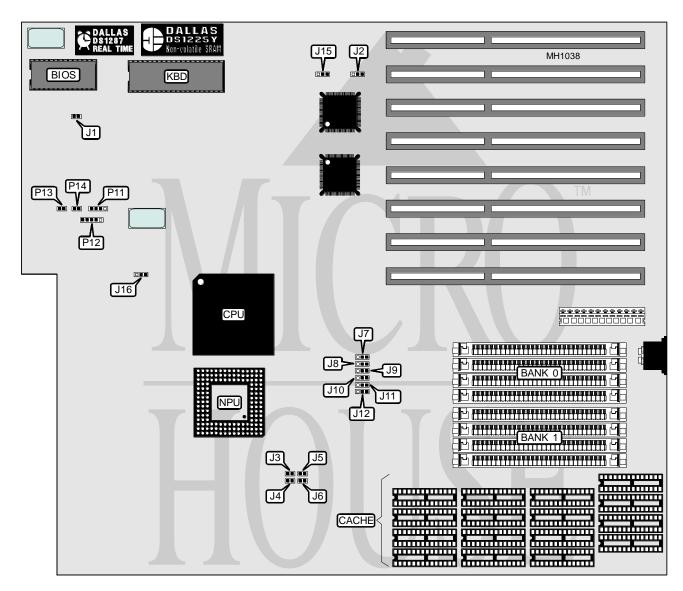
MYLEX CORPORATION

MAE486

Processor 80486DX **Processor Speed** 25/33MHz **Chip Set** Intel Max. Onboard DRAM 32MB Cache 128KB

BIOS Mylex/Phoenix 355mm x 304mm **Dimensions**

I/O Options None **NPU Options** 4167



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CONNECTIONS				
Purpose	Location	Purpose	Location	
Speaker	P11	Turbo LED	P13	
Power LED & keylock	P12	Reset switch	P14	

USER CONFIGURABLE SETTINGS					
Function	Jumper	Position			
í Cache enabled	J1	Closed			
Cache disabled	J1	Open			
í CPU speed select 33MHz	J2	pins 1 & 2 closed			
CPU speed select 25MHz	J2	pins 2 & 3 closed			
í Factory configured - do not alter	J15	pins 2 & 3 closed			
í Factory configured - do not alter	J16	pins 2 & 3 closed			

DRAM CONFIGURATION					
Size	Bank 0	Bank 1			
1MB	(4) 256K x 9	NONE			
2MB	(4) 256K x 9	(4) 256K x 9			
4MB	(4) 1M x 9	NONE			
8MB	(4) 1M x 9	(4) 1M x 9			
16MB	(4) 4M x 9	NONE			
32MB	(4) 4M x 9	(4) 4M x 9			

DRAM JUMPER CONFIGURATION										
Size	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12
1MB	Open	Open	Open	Open	Open	2 & 3	Open	1 & 2	1 & 2	1 & 2
2MB	Closed	Closed	Closed	Closed	Open	1 & 2	Open	1 & 2	1 & 2	2 & 3
4MB	Open	Open	Open	Open	Open	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3
8MB	Closed	Closed	Closed	Closed	Open	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3
16MB	Open	Open	Open	Open	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3
32MB	Closed	Closed	Closed	Closed	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.										

CACHE CONFIGURATION				
Size	Bank 0			
128KB	(16) 8K x 8			