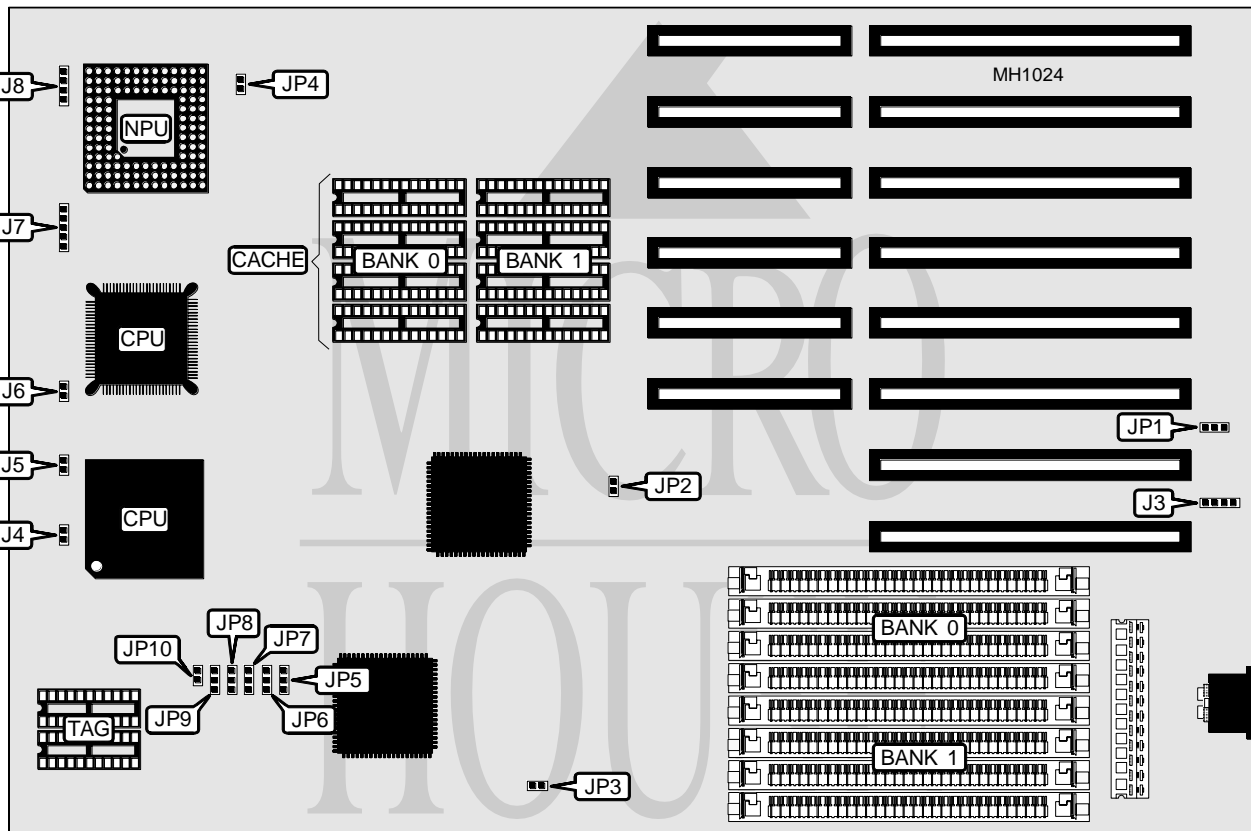


# AMPTRON INTERNATIONAL, INC.

## DX - 5400

<b>Processor</b>	80386DX
<b>Processor Speed</b>	33/40MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	32/64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Reset switch	J6
Turbo switch	J4	Power LED & keylock	J7
Turbo LED	J5	Speaker	J8

Continued on next page . . .

# AMPTRON INTERNATIONAL, INC.

## DX - 5400

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	Open
Monitor type select monochrome	JP2	Closed
í Bus speed clock select OSCIN/4	JP3	Closed
Bus speed clock select OSCIN/3	JP3	Open
í NPU mode select synchronous with CPU	JP4	Open
NPU mode select synchronous with oscillator	JP4	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(2) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(2) 16K x 4
128KB	(4) 32K x 8	NONE	(2) 16K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(2) 16K x 4

CACHE JUMPER CONFIGURATION						
Size	JP5	JP6	JP7	JP8	JP9	JP10
32KB	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	Open
64KB	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2	Open
128KB	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	Closed
256KB	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2	Closed

Note: Pins designated should be in the closed position.