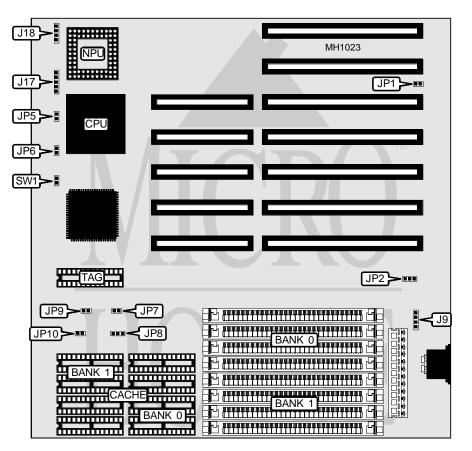
AMPTRON INTERNATIONAL, INC. D X - 5 5 0 0

Processor	80386
Processor Speed	40MH
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/12
BIOS	AMI
Dimensions	330m
I/O Options	None
NPU Options	80387

80386DX 40MHz OPTI 32MB 64/128/256KB AMI 330mm x 218mm None 80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	19	Turbo switch	JP5
Speaker	J17	Turbo LED	JP6
Power LED & keylock	J18	Reset switch	SW1

Continued on next page . . .

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Monitor type select color	JP1	Open	
Monitor type select monochrome	JP1	Closed	
í CMOS memory normal operation	JP2	pins 2 & 3 closed	
CMOS memory clear	JP2	pins 1 & 2 closed	

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size Bank 0 Bank 1 TAG			TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP7	JP8	JP9	JP10
64KB	Open	pins 2 & 3 closed	Open	Open
128KB	Closed	pins 1 & 2 closed	Open	Closed
256KB	Closed	pins 2 & 3 closed	Closed	Closed