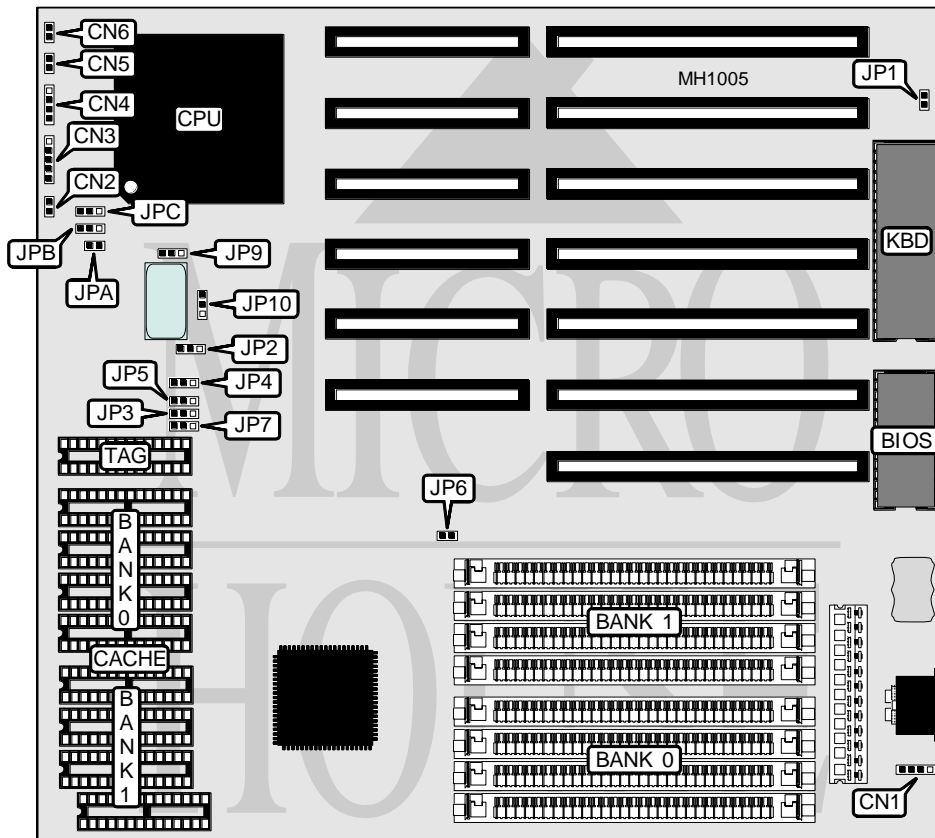


SHUTTLE COMPUTER INTERNATIONAL, INC.

HOT-403H

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	25/33/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	240mm x 220mm
I/O Options	None
NPU Options	None



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	CN1	Speaker	CN4
Turbo LED	CN2	Turbo switch	CN5
Power LED & keylock	CN3	Reset switch	CN6

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HOT-403H

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	CN1	pins 2 & 3 closed
Batter type select external	CN1	Closed
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í CPU speed select iOSC/2 (See CPU SPEED table below)	JP2	pins 2 & 3 closed
CPU speed select iOSC/1 (See CPU SPEED table below)	JP2	pins 1 & 2 closed
í Bus speed select ATCLK/6	JP6	Closed
Bus speed select ATCLK/4	JP6	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP3	JP4	JP5	JP7
64KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
128KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION			
Type	JPA	JPB	JPC
80486SX	Open	pins 1 & 2 closed	Open
80487SX	Closed	pins 2 & 3 closed	pins 2 & 3 closed
80486DX	Closed	pins 2 & 3 closed	pins 1 & 2 closed
80486DX2	Closed	pins 2 & 3 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION			
Speed	JP2	JP9	JP10
25MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
50iMHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
50MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
66iMHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed

