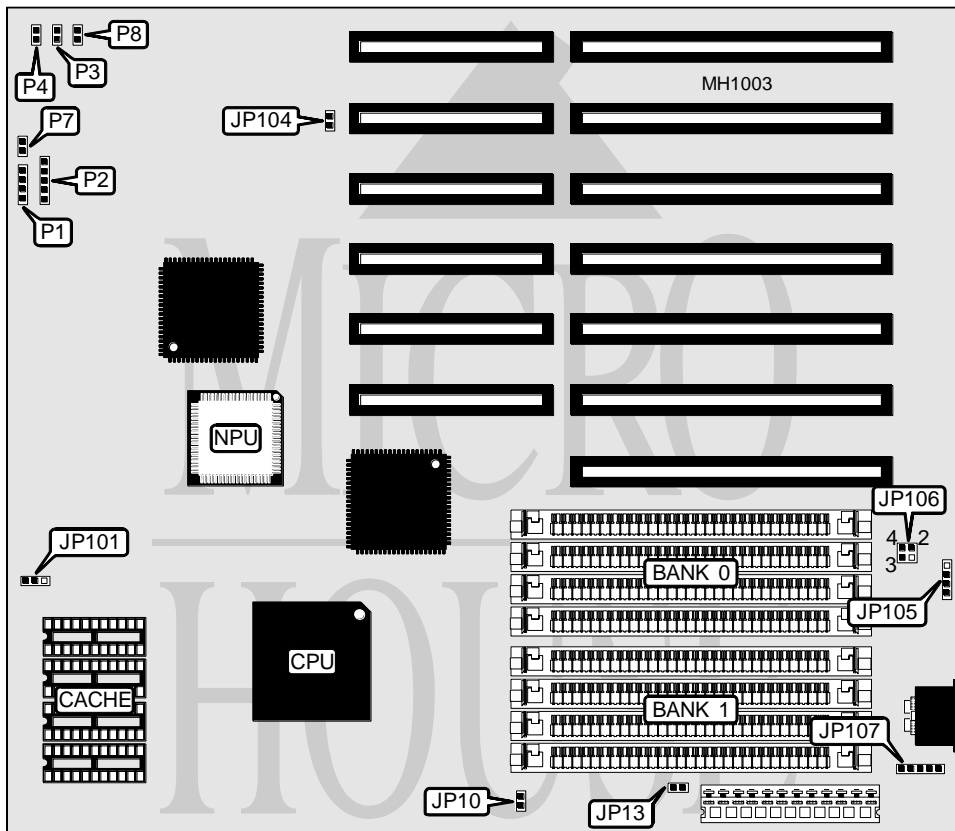


INFORMTECH INTERNATIONAL, INC.

IT386LM CACHE

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	Headland
Max. Onboard DRAM	32MB
Cache	32/128KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP105	Turbo LED	P3
Auxiliary keyboard	JP107	Reset switch	P7
Speaker	P1	Turbo switch	P8
Power LED & keylock	P2		

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IT386LM CACHE

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í I/O recovery delay (for slower cards) disabled	JP13	Open
I/O recovery delay (for slower cards) enabled	JP13	Closed
í CPU speed select 33MHz	JP101	pins 1 & 2 closed
CPU speed select 40MHz	JP101	pins 2 & 3 closed
í NPU mode select synchronous with CPU	JP102	Open
NPU mode select asynchronous with CPU	JP102	Closed
í CMOS memory normal operation	JP104	Open
CMOS memory clear	JP104	Closed
í Battery select internal 3.6VDC with current limiting resistor	JP106	pins 1 & 3 closed
Battery select internal 4.5VDC alkaline	JP106	pins 3 & 4 closed
Battery select external 6VDC	JP106	pins 2 & 4 closed
Battery select external 6VDC with current limiting resistor	JP106	pins 1 & 2 closed
í Monitor type select color	P4	Closed
Monitor type select monochrome	P4	Open

Note: Jumpers JP101 and JP102 may not be present on all board revisions.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION	
Size	Bank 0
32KB	(4) 8K x 8
128KB	(4) 32K x 8

CACHE JUMPER CONFIGURATION	
Size	JP10
32KB	Closed
128KB	Open

Note: Jumper JP10 may be named JP100 on some board revisions.