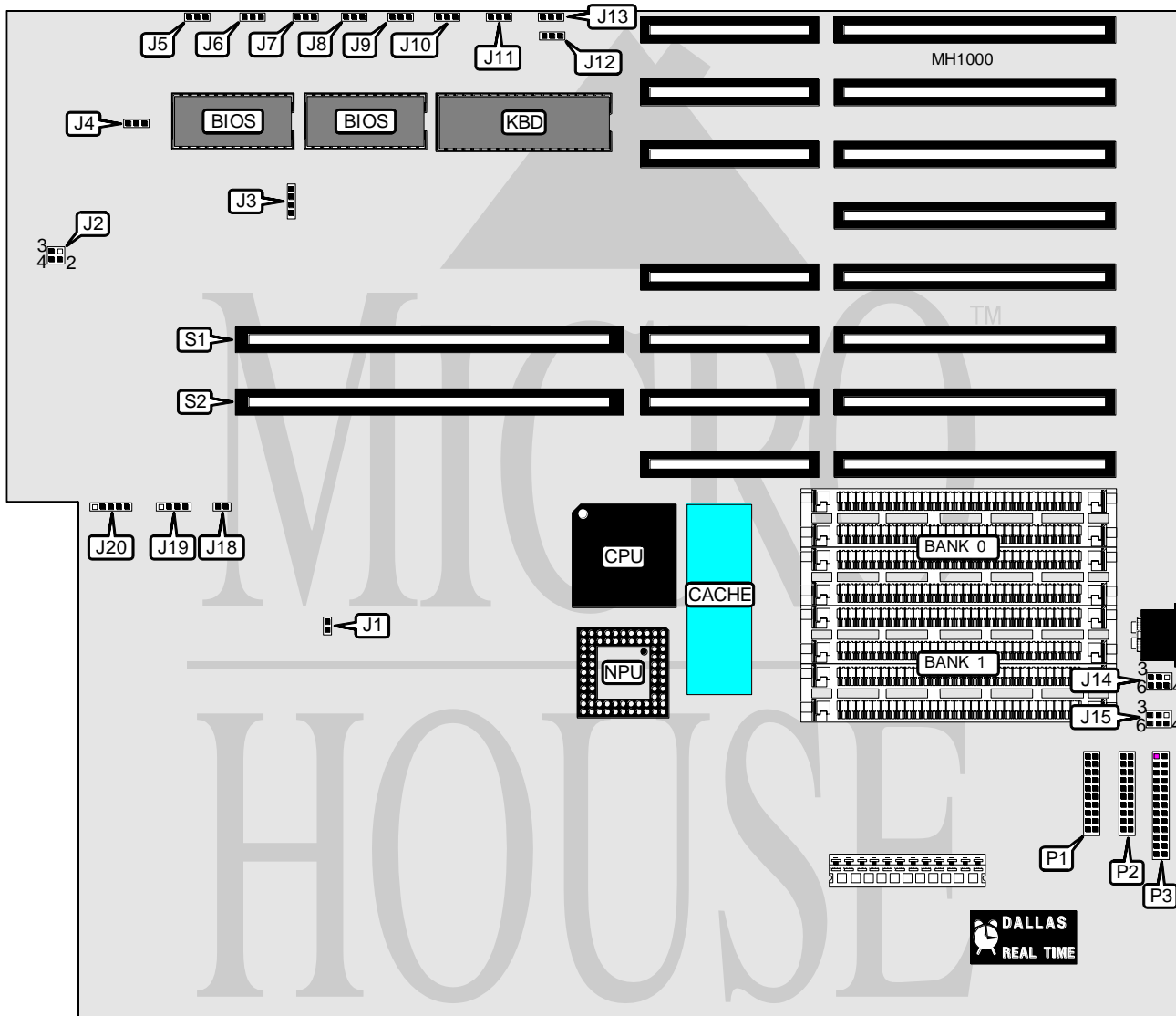


SIEMENS NIXDORF INFORMATIONSSYSTEME AG

386 MODEL 302

Processor	80386DX
Processor Speed	25MHz
Chip Set	Unidentified
Max. Onboard DRAM	8MB
Cache	64KB
BIOS	Phoenix
Dimensions	355mm x 304mm
I/O Options	32-bit external memory card slots (2), parallel port, serial ports (2)
NPU Options	80387



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	P1	Reset switch	J18
Serial port 1	P2	Power LED & keylock	J20
Parallel port	P3	32-bit external memory card	S1
Speaker	J19	32-bit external memory card	S2

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J1	Closed
í DRAM SIMM type select static column	J3	pins 3 & 4 closed
DRAM SIMM type select fast paged mode	J3	pins 1 & 2, 3 & 4 closed
DRAM SIMM type select standard RAS/CAS	J3	Open
í BIOS type select 27256	J4	pins 1 & 2 closed
BIOS type select 27512	J4	pins 2 & 3 closed
í Monitor type select color	J5	pins 2 & 3 closed
Monitor type select monochrome	J5	pins 1 & 2 closed
í Operating system select non-UNIX	J6	pins 1 & 2 closed
Operating system select UNIX	J6	pins 2 & 3 closed
í Keyboard reset enabled	J7	pins 1 & 2 closed
Keyboard reset disabled	J7	pins 2 & 3 closed
í NPU absent	J8	pins 1 & 2 closed
NPU present	J8	pins 2 & 3 closed
í Serial port 2 enabled	J9	pins 1 & 2 closed
Serial port 2 disabled	J9	pins 2 & 3 closed
í Serial port 1 enabled	J10	pins 1 & 2 closed
Serial port 1 disabled	J10	pins 2 & 3 closed
í Printer acknowledge select trailing edge	J12	pins 2 & 3 closed
Printer acknowledge select leading edge	J12	pins 1 & 2 closed
í Factory configured - do not alter (100ns SIMM)	J14	pins 1 & 2, 4 & 5 closed
SIMM type select 256K x 9	J15	pins 1 & 2, 4 & 5 closed
SIMM type select 1M x 9	J15	pins 2 & 3, 5 & 6 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9

DRAM JUMPER CONFIGURATION	
Size	J2
1MB	Open
2MB	pins 2 & 4 closed
4MB	pins 1 & 3 closed
8MB	pins 1 & 3, 2 & 4 closed

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PARALLEL PORT CONFIGURATION		
Setting	J11	J13
LPT1 enabled & LPT2 disabled	pins 1 & 2 closed	pins 2 & 3 closed
LPT2 enabled & LPT1 disabled	pins 2 & 3 closed	pins 1 & 2 closed