## ANIX TECHNOLOGY CORPORATION

## MST-310M

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

80386DX
$33 / 40 \mathrm{MHz}$
UMC
32 MB
64/128/256KB
AMI
$330 \mathrm{~mm} \times 218 \mathrm{~mm}$
None
80387/3167


| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| Speaker | J8 pins 1-4 | Turbo switch | J8 pins 15-16 |
| Power LED \& keylock | Jurbo LED | $\mathrm{J} 8 /$ pins 18-19 |  |
| Reset switch | J8 pins 11-12 |  |  |
| Note : CPU may be supplied as either surface mounted or socketed. |  |  |  |

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| USER CONFIGURABLE SETTINGS |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Function |  |  |  | Jumper | Position |
| í NPU type select 80387 | W6 and W7 | closed |  |  |  |
| NPU type select 3167 | W6 and W7 | open |  |  |  |
| í Monitor type select monochrome | W9 | open |  |  |  |
| Monitor type select color | W9 | closed |  |  |  |
| í CMOS memory normal operation | W11 | pins 2 \& 3 closed |  |  |  |
| CMOS memory clear | W11 | pins $1 \& 2$ closed |  |  |  |


| DRAM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 |
| 1 MB | $(4) 256 \mathrm{~K} \times 9$ | NONE |
| 2 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ |
| 4 MB | (4) $1 \mathrm{M} \times 9$ | NONE |
| 5 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $1 \mathrm{M} \times 9$ |
| 8 MB | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ |
| 16 MB | (4) $4 \mathrm{M} \times 9$ | NONE |
| 20 MB | (4) $1 \mathrm{M} \times 9$ | (4) $4 \mathrm{M} \times 9$ |
| 32 MB | (4) $4 \mathrm{M} \times 9$ | (4) $4 \mathrm{M} \times 9$ |


| CACHE CONFIGURATION |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 | TAG (U4) | TAG (UX4) |
| 64 KB | (4) $8 \mathrm{~K} \times 8$ | $(4) 8 \mathrm{~K} \times 8$ | $(1) 8 \mathrm{~K} \times 8$ | NONE |
| 128 KB | (4) $32 \mathrm{~K} \times 8$ | NONE | (1) $8 \mathrm{~K} \times 8$ | NONE |
| 256 KB | (4) $32 \mathrm{~K} \times 8$ | (4) $32 \mathrm{~K} \times 8$ | (1) $8 \mathrm{~K} \times 8$ | $(1) 8 \mathrm{~K} \times 8$ |
| 256 KB | (4) $32 \mathrm{~K} \times 8$ | (4) $32 \mathrm{~K} \times 8$ | $(1) 32 \mathrm{~K} \times 8$ | NONE |


| CACHE JUMPER CONFIGURATION |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Size | JP1 | JP2 | WC1 | WC2 | WC3 | WC4 | WC5 |
| $64 K B$ | Closed | $1 \& 2$ | $2 \& 3$ | Open | Open | $1 \& 2$ | $1 \& 2$ |
| 128 KB | Open | $2 \& 3$ | $1 \& 2$ | Open | Closed | $2 \& 3$ | $1 \& 2$ |
| 256 KB | Open | $2 \& 3$ | $1 \& 2$ | Closed | Closed | $2 \& 3$ | $2 \& 3$ |
| 256 KB | Closed | $1 \& 2$ | $2 \& 3$ | Closed | Closed | $2 \& 3$ | $2 \& 3$ |

