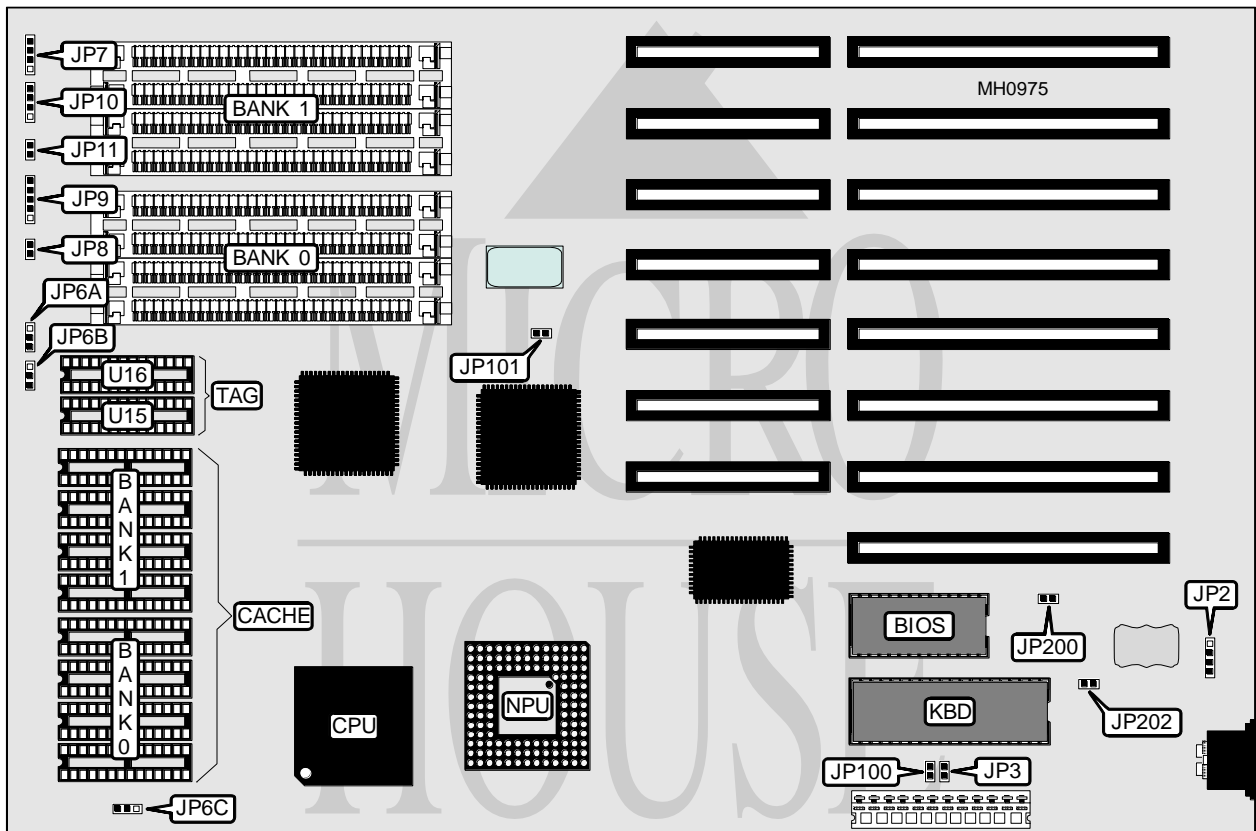


SPRITE, INC.

SM 386SX-20C

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	Forex
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI/MR
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Power LED & keylock	JP9
Turbo LED	JP7	Speaker	JP10
Reset switch	JP8	Turbo switch	JP11

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SM 386SX-20C

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP3	Closed
Monitor type select monochrome	JP3	Open
í Factory configured - do not alter	JP100	Open
í Parity check enabled	JP101	Closed
Parity check disabled	JP101	Open
í External battery select 3.6VDC	JP200	Closed
External battery select 6.0VDC	JP200	Open
í CMOS memory normal operation	JP202	Open
CMOS memory clear	JP202	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U15)	TAG (U16)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	NONE
128KB	(4) 32K x 8	NONE	(1) 8K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 8K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION			
Size	JP6A	JP6B	JP6C
64KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
128KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed