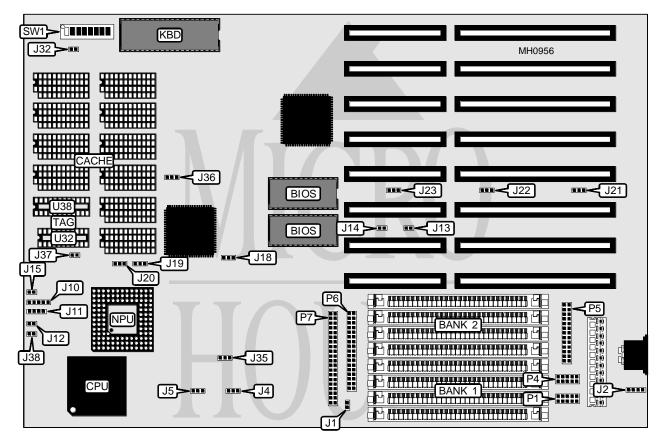
AMERICAN MEGATRENDS, INC. MARK V BABY SCREAMER

Processor Processor Speed	80386DX 25/33/40MHz
Chip Set	AMI
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2)
NPU Options	80387/3167
	000070107



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface LED	J1	Turbo switch	J38
External battery	J2	Serial port 2	P1
Power LED & keylock	J10	Serial port 1	P4
Speaker	J11	Parallel port	P5
Reset switch	J12	Floppy interface	P6
Diagnostic LED	J13	IDE interface	P7
Turbo LED	J15		

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
í ROM access select 0 wait state	J14	Closed	
ROM access select 1 wait state	J14	Open	
í BIOS type select 27256	J18	pins 1 & 2 closed	
BIOS type select 27512	J18	pins 2 & 3 closed	
í Serial port COM2 select IRQ3	J21	pins 2 & 3 closed	
Serial port COM2 select IRQ4	J21	pins 1 & 2 closed	
í Serial port COM1 select IRQ4	J22	pins 2 & 3 closed	
Serial port COM1 select IRQ3	J22	pins 1 & 2 closed	
í Parallel port IRQ select IRQ7	J23	pins 2 & 3 closed	
Parallel port IRQ select IRQ5	J23	pins 1 & 2 closed	
í Segment C800 shadow enabled	J36	pins 2 & 3 closed	
Segment C800 shadow disabled	J36	pins 1 & 2 closed	
í Factory configured - do not alter	SW1/1	Off	
í Monitor type select monochrome	SW1/2	Off	
Monitor type select color	SW1/2	On	
í Video shadow enabled	SW1/3	On	
Video shadow disabled	SW1/3	Off	
í ROM BIOS shadow enabled	SW1/4	On	
ROM BIOS shadow disabled	SW1/4	Off	

	DRAM CONFIGURATION	
Size	Bank 1	Bank 2
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

DRAM JUMPER/SWITCH CONFIGURATION						
Size	J4	J5	SW1/5	SW1/6	SW1/7	SW1/8
1MB	2&3	2 & 3	On	On	On	On
2MB	2&3	2 & 3	On	Off	On	On
4MB	2&3	1 & 2	On	On	On	Off
8MB	2&3	1 & 2	On	Off	On	Off
16MB	1 & 2	1 & 2	On	On	Off	Off
32MB	1 & 2	1 & 2	On	Off	Off	Off
Note: Pins designated should be in the closed position						

note : Pins designated should be in the closed position.

CACHE CONFIGURATION			
Size	Bank 0	TAG (U32)	TAG (U38)
64KB	(10) 16K x 4	(1) 16K x 4	NONE
256KB	(10) 64K x 4	NONE	(1) 64K x 4

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CACHE JUMPER CONFIGURATION			
Size	J19	J20	J32
64KB	pins 2 & 3 closed	pins 2 & 3 closed	Open
256KB	pins 1 & 2 closed	pins 1 & 2 closed	Closed

	CPU SPEED CONFIGURATION	
Speed	J35	J37
25MHz	pins 2 & 3 closed	Open
33MHz	pins 2 & 3 closed	Open
40MHz	pins 1 & 2 closed	Closed