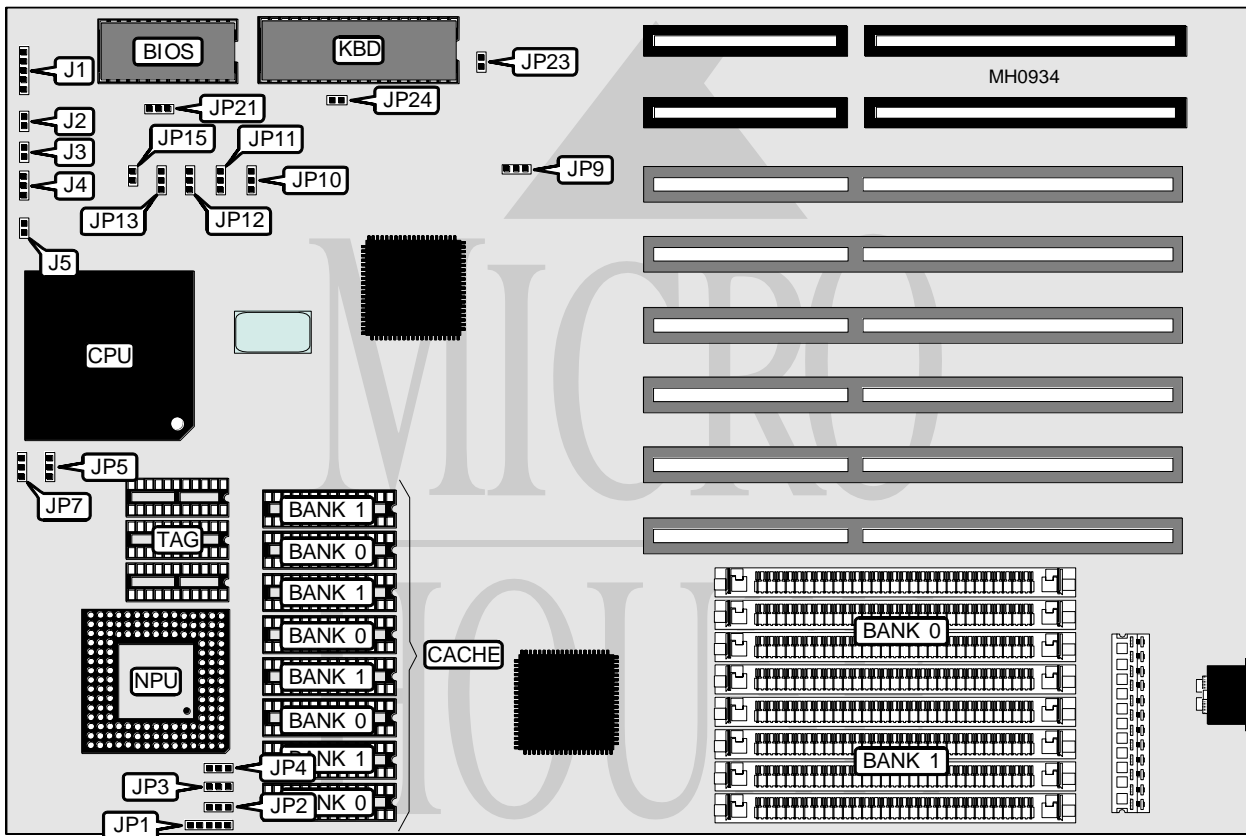


AUSTIN COMPUTER SYSTEMS

486 EISA SI-6

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	33/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	32MB (128MB with BIOS upgrade)
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo switch	J4
Speaker	J2	Turbo LED	J5
Reset switch	J3		

Continued on next page . . .

AUSTIN COMPUTER SYSTEMS

486 EISA SI-6

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP9	pins 1 & 2 closed
í Factory configured - do not alter	JP21	pins 1 & 2 closed
í Burst mode select enabled	JP23	Closed
Burst mode select disabled	JP23	Open
í Monitor type select color	JP24	Open
Monitor type select monochrome	JP24	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	None
128MB	(4) 16M x 9	(4) 16M x 9

Note : The main board will only accept 16M x 9 SIMMs if the BIOS upgrade is installed.

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8

Note : Tag chips are factory installed and are not configurable.

CACHE JUMPER CONFIGURATION						
Size	JP1	JP2	JP3	JP4	JP12	JP13
64KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
128KB	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
256KB	4 & 5	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION			
Type	JP5	JP7	JP15
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	Open
80487SX	pins 2 & 3 closed	pins 1 & 2 closed	Closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	Closed

BATTERY TYPE CONFIGURATION		
Type	JP10	JP11
DS1287	pins 1 & 2 closed	pins 1 & 2 closed
DS1488	pins 2 & 3 closed	pins 2 & 3 closed