## AMERICAN MEGATRENDS, INC.

## 386/486 MARK III CONVERTIBLE

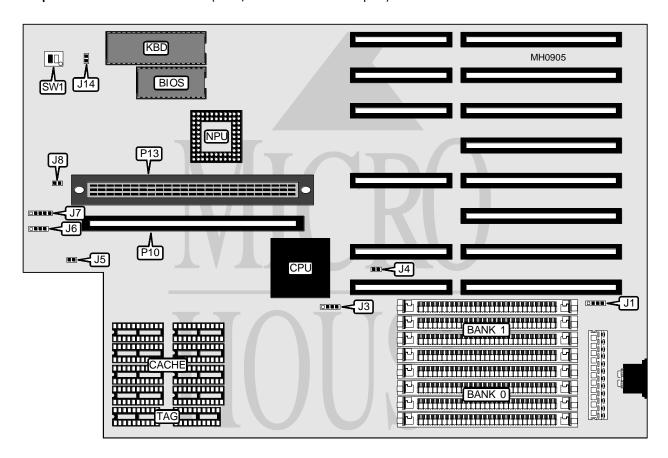
80386DX (80486DX on MARK III adapter) **Processor** 

**Processor Speed** 25/33MHz **Chip Set** AMI Max. Onboard DRAM 32MB Cache 64KB **BIOS** AMI

**Dimensions** 330mm x 218mm

I/O Options 32-bit external memory card slot, CPU/NPU expansion card slot

**NPU Options** 80387 (3167/4167 on MARK III adapter)



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	J1	Turbo switch	J8	
Turbo LED	J5	Reset switch	J14	
Speaker	J6	32-bit external memory card	P10	
Power LED & keylock	J7	CPU/NPU expansion card	P13	

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USER CONFIGURABLE SETTINGS				
Function	Jumper/Switch	Position		
DRAM address sequence select bank 0, then bank 1	J3	pins 1 & 2, 3 & 4 closed		
DRAM address sequence select bank 1, then bank 0	J3	pins 2 & 3, 4 & 5 closed		
í CMOS memory normal operation	J4	Open		
CMOS memory clear	J4	Closed		
í Factory configured - do not alter	SW1/switch 1	Off		
Monitor type select color	SW1/switch 2	On		
Monitor type select monochrome	SW1/switch 2	Off		
Note: Cache is factory installed and is not configurable. The size of the cache & tag are unidentified.				

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
5MB	(4) 256K x 9	(4) 1M x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
20MB	(4) 1M x 9	(4) 4M x 9		
32MB	(4) 4M x 9	(4) 4M x 9		