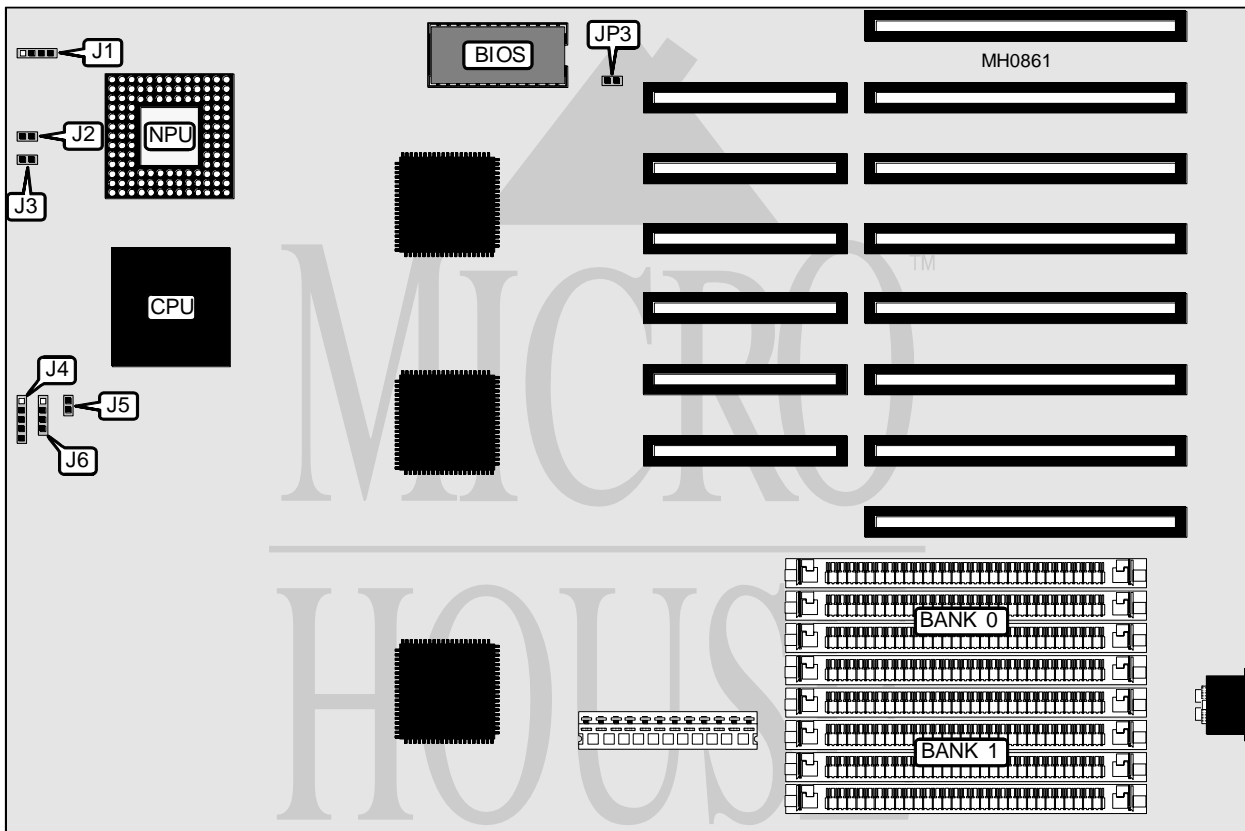


UNIDENTIFIED

CACHE 386-33/25

Processor	80386DX
Processor Speed	25/33MHz
Chip Set	C & T PEAK DM
Max. Onboard DRAM	32MB
Cache	64KB
BIOS	AMI/Award/J-bond
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Power LED & keylock	J4
Turbo LED	J2	Reset switch	J5
Turbo switch	J3	Speaker	J6

Continued on next page . . .

UNIDENTIFIED

CACHE 386-33/25

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP3	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
64KB	(8) 8K x 8	(1) 8K x 8

Note: The location of the cache is unidentified.