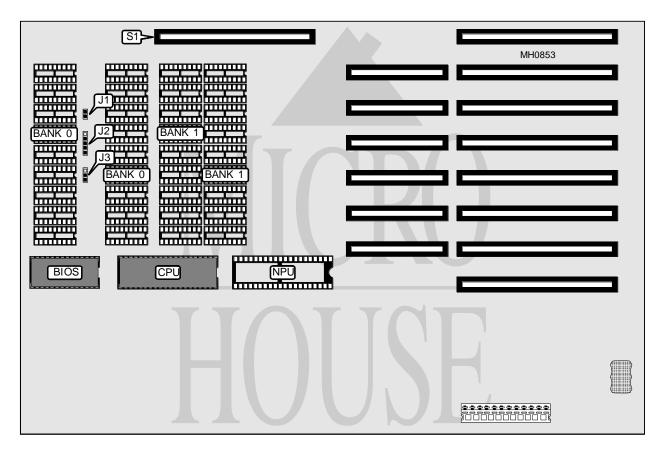
## **PACKARD BELL** PB800/PACK-MATE

**Processor** 80286 **Processor Speed** 12/16MHz C & T **Chip Set** Max. Onboard DRAM 1MB Cache None **BIOS** Phoenix

**Dimensions** 330mm x 218mm

I/O Options External memory card slot

**NPU Options** 80287



CONNECTIONS					
Purpose	Location	Purpose	Location		
Reset switch	J1	Speaker	J3		
Keylock	J2	External memory card	S1		

Continued on next page . . .

## **PACKARD BELL** PB800/PACK-MATE

. . . continued from previous page

USER CONFIGURABLE SETTINGS					
Function	Jumper	Position			
í NPU mode select synchronous with CPU	J19	pins 1 & 2 closed			
NPU mode select asynchronous with CPU	J19	pins 2 & 3 closed			

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3		
512KB	(18) 41256	NONE	NONE	NONE		
1MB	(18) 41256	(18) 41256	NONE	NONE		
2MB	(18) 41256	(18) 41256	(2) 256K x 9	(2) 256K x 9		
3MB	(18) 41256	(18) 41256	(2) 1M x 9	NONE		
5MB	(18) 41256	(18) 41256	(2) 1M x 9	(2) 1M x 9		
6MB	(18) 411000	(18) 411000	(2) 1M x 9	NONE		
8MB	(18) 411000	(18) 411000	(2) 1M x 9	(2) 1M x 9		

Banks 2 & 3 are used when a proprietary external memory card is installed at S1. Note: 6MB and 8MB can be configured using Banks 0 through 3 on the external memory card only.