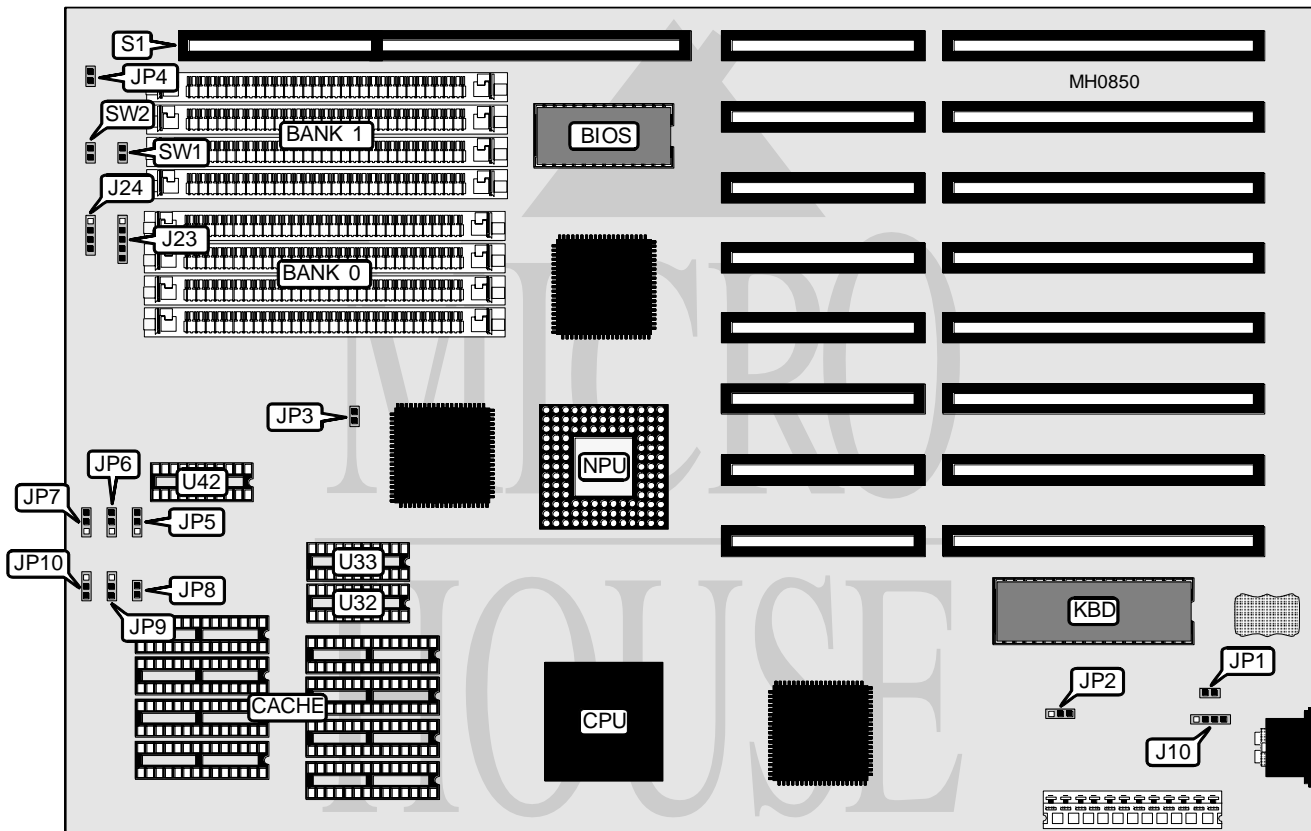


SILICON VALLEY COMPUTER, INC.

3 8 6 W B

Processor 80386DX
Processor Speed 25/33/40MHz
Chip Set C & T
Max. Onboard DRAM 32MB
Cache 64/256KB
BIOS C & T
Dimensions 330mm x 218mm
I/O Options 32-bit external memory card slot
NPU Options 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J10	32-bit external memory card	S1
Power LED & keylock	J23	Reset switch	SW1
Speaker	J24	Turbo switch	SW2
Turbo LED	JP4		

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386WB

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select CGA	JP1	Closed
Monitor type select monochrome, EGA, or VGA	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Bus speed select ATCLK/4	JP3	closed
Bus speed select ATCLK/6	JP3	open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	TAG (U32 & U33)	Dirty Bit (U42)
64KB	(8) 8K x 8	(2) 4K x 4	(1) 16K x 4
256KB	(8) 32K x 8	(2) 16K x 4	(1) 16K x 4

CACHE JUMPER CONFIGURATION						
Size	JP5	JP6	JP7	JP8	JP9	JP10
64KB	2 & 3	1 & 2	1 & 2	Open	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	Closed	1 & 2	1 & 2

Note: Pins designated should be in the closed position.