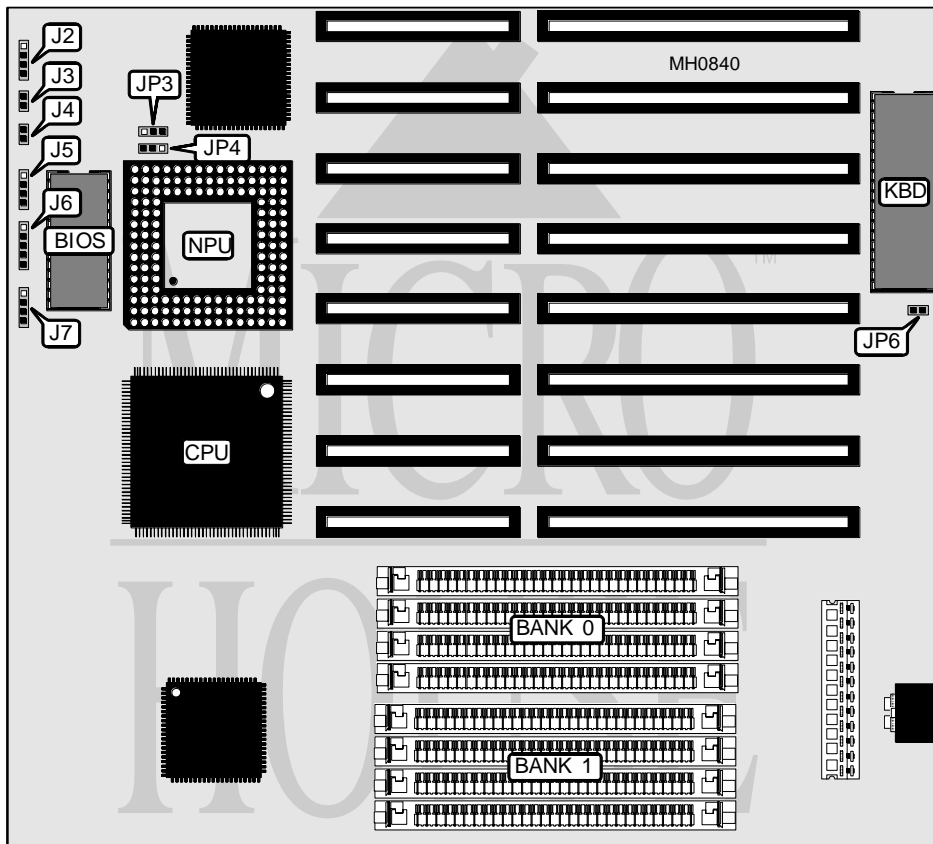


PIONEER COMPUTER, INC.

VANTAGE 486NC SX-20/25/33

Processor	80486SX/ODP486SX
Processor Speed	20/25/33/40(internal)/50(internal)/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	None
BIOS	Phoenix
Dimensions	240mm x 220mm
I/O Options	None
NPU Options	80487SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J2	Speaker	J5
Turbo switch	J3	Power LED & keylock	J6
Reset switch	J4	External battery	J7

Continued on next page . . .

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 VANTAGE 486NC SX-20/25/33

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP3	pins 1 & 2 closed
CMOS memory clear	JP3	pins 2 & 3 closed
í 80487SX or ODP486SX upgrade not installed	JP4	pins 1 & 2 closed
80487SX or ODP486SX upgrade installed	JP4	pins 2 & 3 closed
í Factory configured - do not alter	JP6	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9