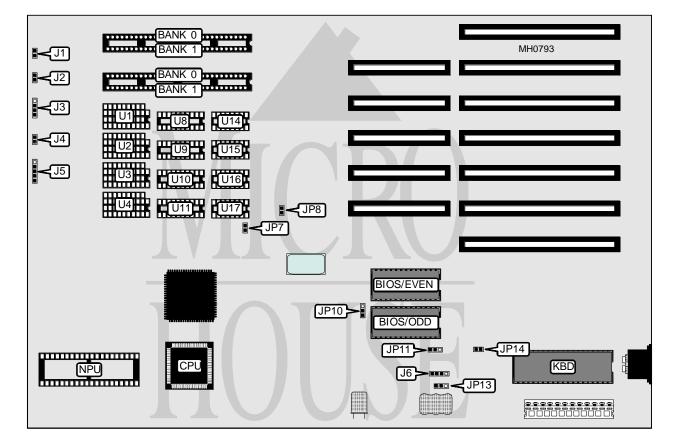
AUVA COMPUTER, INC. BAM 12-A0(A1)/BAM 16-A0

80286
12/16MHz
ACER
4MB
None
AMI
330mm x 218mm
None
80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J1	Reset switch	J4
Turbo switch	J2	Power LED & keylock	J5
Speaker	J3	External battery	J6

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Parity check enabled	JP7	Open	
Parity check disabled	JP7	Closed	
í Battery select internal	JP11	pins 1 & 2 closed	
Battery select external	JP11	pins 2 & 3 closed	
í CMOS memory normal operation	JP13	pins 1 & 2 closed	
CMOS memory clear	JP13	pins 2 & 3 closed	
í Monitor type select color	JP14	Closed	
Monitor type select monochrome	JP14	Open	

DIP DRAM CONFIGURATION (DIP)				
Size	U8 to U11	U14 & U15	U1 to U4	U16 & U17
512KB	(4) 44256	(2) 41256	NONE	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256

SIPP DRAM CONFIGURATION (SIPP)				
Size	Bank 0	Bank 1		
512KB	(2) 256K x 9	NONE		
1MB	(2) 256K x 9	(2) 256K x 9		
2MB	(2) 1M x 9	NONE		
4MB	(2) 1M x 9	(2) 1M x 9		
Note: When SIPP DRAM is used, DIP DRAM is disabled.				

BIOS CONFIGURATION			
Size	JP8	JP10	
27256	Open	Closed	
27512	pins 1 & 2 closed	pins 2 & 3 closed	