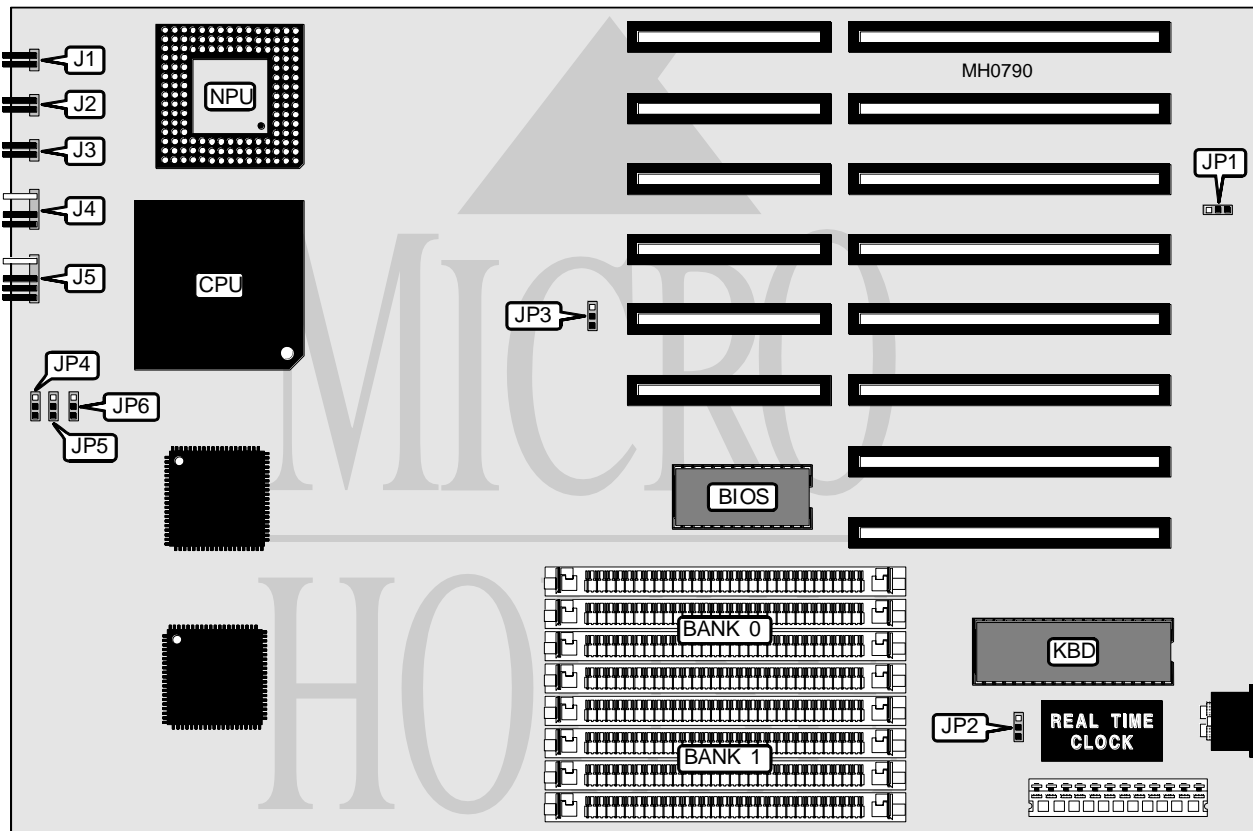


MAGUS DATA TECHNOLOGY, INC.

S 4 8 6

Processor	80486SX/80486DX/80486DX2
Processor Speed	20/25/33/50(internal)/66(internal)MHz
Chip Set	Headland
Max. Onboard DRAM	128MB
Cache	None
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J1	Speaker	J4
Turbo LED	J2	Power LED & keylock	J5
Reset switch	J3		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Power good signal detect from power supply	JP1	pins 1 & 2 closed
Power good signal detect from board	JP1	pins 2 & 3 closed
í Monitor type select color	JP2	pins 1 & 2 closed
Monitor type select monochrome	JP2	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB ¹	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB ¹	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB ¹	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
65MB	(4) 16M x 9	(4) 256K x 9
68MB	(4) 16M x 9	(4) 1M x 9
80MB	(4) 16M x 9	(4) 4M x 9
128MB ¹	(4) 16M x 9	(4) 16M x 9

Note ¹: These configurations support zero wait state reads.

CPU TYPE CONFIGURATION			
Type	JP4	JP5	JP6
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
80487SX	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX2	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION	
Speed	JP3
25MHz	pins 2 & 3 closed
33MHz	pins 1 & 2 closed
50iMHz	pins 2 & 3 closed
66iMHz	pins 1 & 2 closed