MULTI-DIMENSION RESEARCH, INC.
F M S 3 8 6 / 2 5 (1 / 3 B A B Y A T)

Processor	80386DX
Processor Speed	25/33/40MHz
Chip Set	Symphony
Max. Onboard DRAM	32MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387/3167
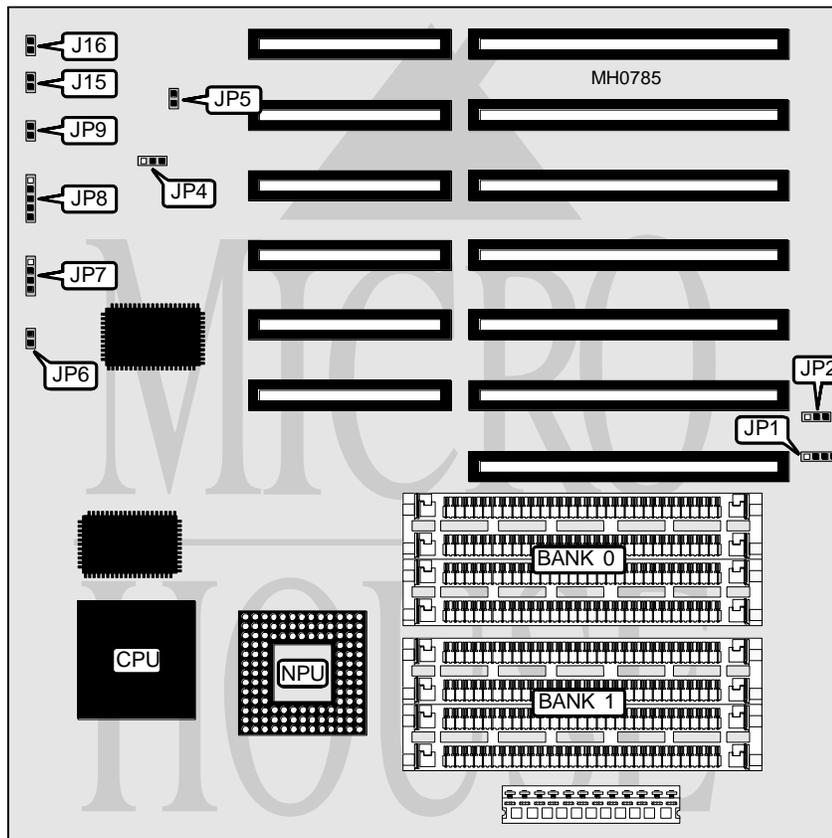
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J15	Reset switch	JP6
Turbo LED	J16	Speaker	JP7
External battery	JP2	Power LED & keylock	JP8

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Internal battery enabled	JP1	pins 1 & 2 closed
Internal battery disabled	JP1	pins 2 & 3 closed
í Factory configured - do not alter	JP4	N/A
í Monitor type select color	JP5	Closed
Monitor type select monochrome	JP5	Open
í ROM access select 1 wait state	JP9	Closed
ROM access select 3 wait state	JP9	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9