

AUVA COMPUTER, INC.

CAM33 - P2 / CPM20 - P0 / CPM25 - P0

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í Bus speed select CLKI/4	JP6	Open
Bus speed select CLKI/6	JP6	Closed
í CPU speed select iOSC/2	JP7	pins 2 & 3 closed
CPU speed select iOSC/1	JP7	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(1) 1M x 36	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 256K x 9	(4) 1M x 9	(1) 1M x 36	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(1) 1M x 36	(1) 1M x 36
12MB	(4) 1M x 9	(4) 1M x 9	(1) 1M x 36	NONE
13MB	(4) 256K x 9	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	(4) 1M x 9	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(1) 4M x 36	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(1) 1M x 36	(1) 4M x 36
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(1) 4M x 36	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	(4) 4M x 9	(1) 4M x 36	NONE
52MB	(4) 1M x 9	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36
64MB	(4) 4M x 9	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36

Note: Banks 2 & 3 are not on all boards and have to be originally ordered from the manufacturer.

CACHE CONFIGURATION			
Size	Bank 0	TAG	Dirty Bit
64KB	(8) 8K x 8	(1) 8K x 8	(1) 64K x 1
256KB	(8) 32 K x 8	(1) 32K x 8	(1) 64K x 1

CACHE JUMPER CONFIGURATION				
Size	JP8	JP9	JP10	JP12
64KB	Open	Open	Open	Open
256KB	Open	Closed	Closed	Closed

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CPU TYPE CONFIGURATION			
Type	JP3	JP4	JP5
80486SX	Open	Open	pins 2 & 3 closed
80486DX	pins 2 & 3 closed	Closed	pins 1 & 2 closed