# WYSE TECHNOLOGY, INC.
# WYSE PC 286 MODEL 2116I

| | |
|---|---|
| **Processor** | 80286 |
| **Processor Speed** | 16MHz |
| **Chip Set** | WYSE |
| **Max. Onboard DRAM** | 4MB |
| **Cache** | None |
| **BIOS** | WYSE |
| **Dimensions** | 334mm x 122mm |
| **I/O Options** | None |
| **NPU Options** | 80287 |



| CONNECTIONS | | | |
|---|---|---|---|
| Purpose | Location | Purpose | Location |
| External battery | P2 | Power good/Speaker | P6 |
| Front panel connector | P5 | Card edge connector | S1 |
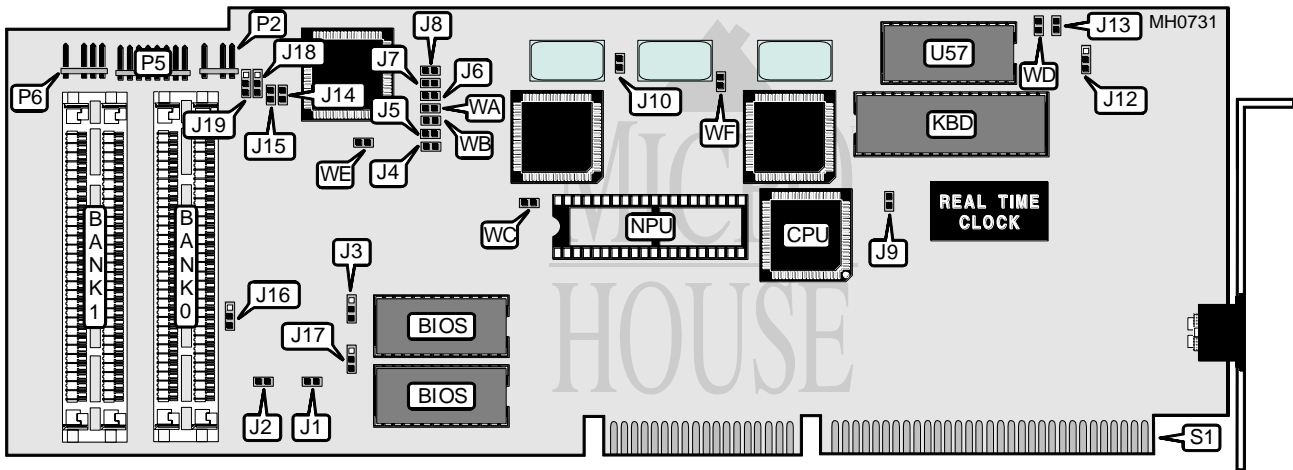
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| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| Function | Jumper | Position |
| í  Factory configured - do not alter | J3 | pins 1 & 2 closed |
| í  DRAM wait state select 0 | J6 | Closed |
|    DRAM wait state select 1 | J6 | Open |
| í  Parity check enabled | J9 | Closed |
|    Parity check disabled | J9 | Open |
| í  Factory configured - do not alter | J10 | Open |
| í  Keyboard scanner chip at U57 select PROM chip | J12 | pins 1 & 2 closed |
|    Keyboard scanner chip at U57 select EPROM chip | J12 | pins 2 & 3 closed |
| í  Factory test disabled | J13 | Open |
|    Factory test enabled | J13 | Closed |
| í  Factory configured - do not alter | J17 | pins 2 & 3 closed |
| í  Chipset select LB ASIC 211008-02 | J18 and J19 | pins 2 & 3 closed |
|    Chipset select LB ASIC 211008-01 | J18 and J19 | pins 1 & 2 closed |
| í  Factory configured - do not alter | WC | Open |
| í  Monitor type select color | WD | Closed |
|    Monitor type select monochrome | WD | Open |
| í  Factory configured - do not alter | WE | Closed |
| í  Factory configured - do not alter | WF | Open |

| DRAM CONFIGURATION | | |
|---|---|---|
| Size | Bank 0 | Bank 1 |
| 1MB | (2) 256K x 9 | (2) 256K x 9 |
| 2MB | (2) 1M x 9 | NONE |
| 4MB | (2) 1M x 9 | (2) 1M x9 |

| DRAM JUMPER CONFIGURATION | | | | |
|---|---|---|---|---|
| Size | J5 | J14 | J15 | J16 |
| 1MB | Closed | Closed | Open | pins 2 & 3 closed |
| 2MB | Open | Open | Closed | pins 1 & 2 closed |
| 4MB | Open | Open | Closed | pins 1 & 2 closed |

| MEMORY SPLIT CONFIGURATION | | | |
|---|---|---|---|
| Memory Split | J4 | WA | WB |
| 256KB/786KB | Closed | N/A | Open |
| 512KB/512KB | Open | Closed | Open |
| 640KB/384KB | Open | Open | Open |
| Disabled | Open | N/A | Closed |

| BIOS CONFIGURATION | | |
|---|---|---|
| Type | J1 | J2 |
| 27128 | Open | Open |
| 27256 | Closed | Open |
| 27512 | Closed | Closed |

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| BIOS WAIT STATE CONFIGURATION | | |
|---|---|---|
| Wait state | J7 | J8 |
| 0 | Closed | Open |
| 1 | Open | Open |
| 2 | N/A | Closed |