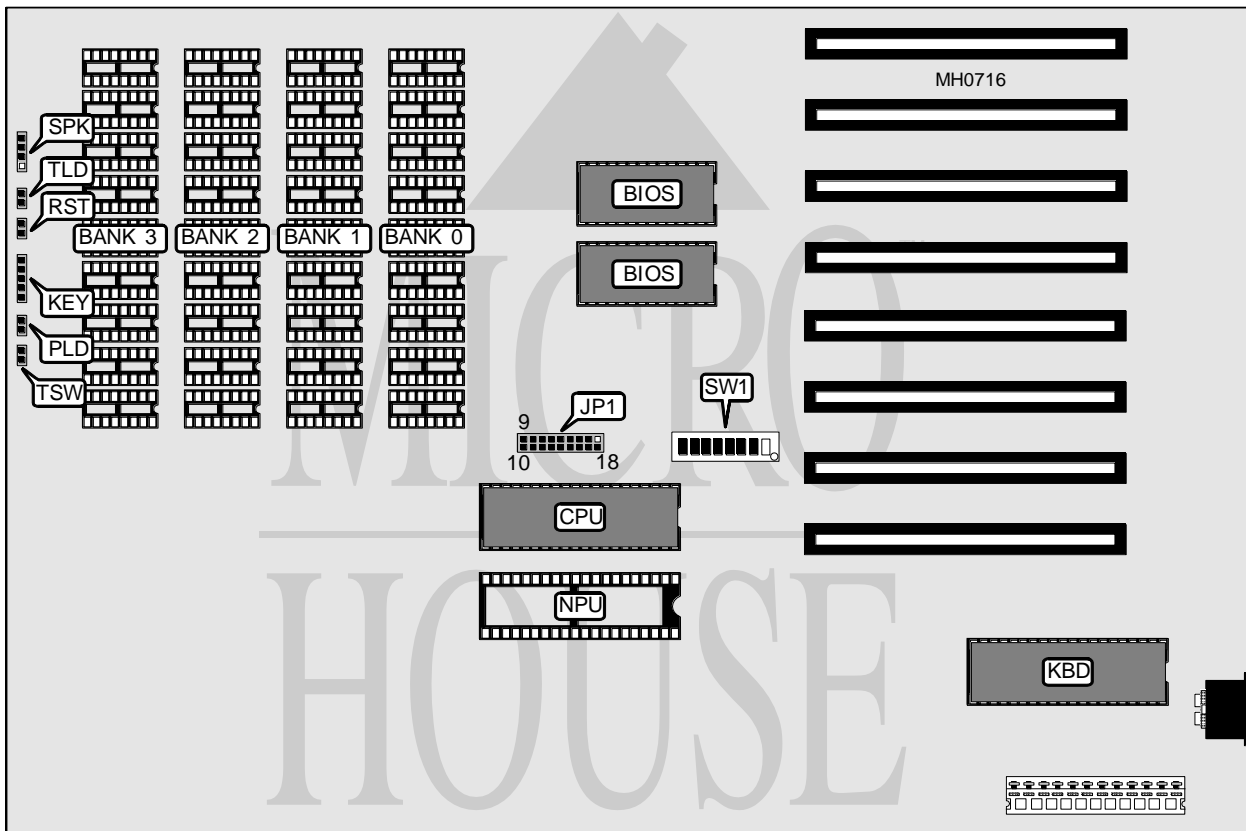


# AUVA COMPUTER, INC.

## TXM10-II (M8031)/TXM8-III (M8021)

<b>Processor</b>	8088
<b>Processor Speed</b>	8/10MHz
<b>Chip Set</b>	None
<b>Max. Onboard DRAM</b>	1MB
<b>Cache</b>	None
<b>BIOS</b>	Phoenix
<b>Dimensions</b>	304mm x 215mm
<b>I/O Options</b>	None
<b>NPU Options</b>	8087



CONNECTIONS			
Purpose	Location	Purpose	Location
Keylock	KEY	Speaker	SPK
Power LED	PLD	Turbo LED	TLD
Reset switch	RST	Turbo switch	TSW

Continued on next page . . .

## AUVA COMPUTER, INC.

## TXM10-II (M8031)/TXM8-III (M8021)

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USER CONFIGURABLE SETTINGS		
Function	SW1	Position
í Factory configured - do not alter	SW1/1	Off
í NPU disabled	SW1/2	On
NPU enabled	SW1/2	Off
í Factory configured - do not alter	SW1/3	Off
í Factory configured - do not alter	SW1/4	Off

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
256KB	(9) 4164	(9) 4164	(9) 4164	(9) 4164
448KB	(9) 41256	(9) 4164	(9) 4164	(9) 4164
640KB	(9) 41256	(9) 41256	(9) 4164	(9) 4164
1MB	(9) 41256	(9) 41256	(9) 41256	(9) 41256

DRAM JUMPER CONFIGURATION		
Size	JP1/pins 8 & 11	JP1/pins 9 & 10
256KB	Closed	Closed
448KB	Open	Closed
640KB	Closed	Open
1MB	Open	Open

FLOPPY DRIVE CONFIGURATION		
Drives installed	SW1/7	SW1/8
1	On	On
2	Off	On
3	On	Off
4	Off	Off

MONITOR CONFIGURATION		
Type	SW1/5	SW1/6
Monochrome	Off	Off
40 x 20 color	Off	On
80 x 25 color	On	Off
EGA	On	On