## FRONTIER INDUSTRIAL, INC.

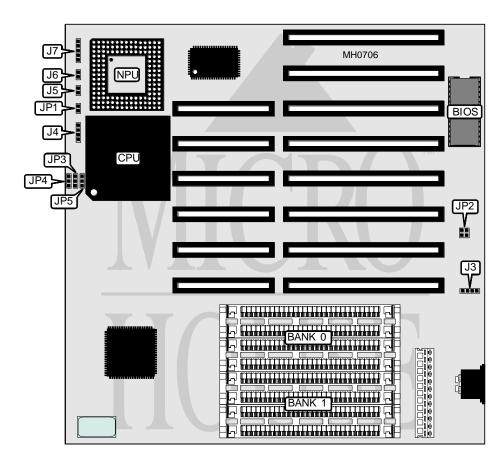
## VLSI 82C486

**Processor** 80486SX/80486DX **Processor Speed** 16/20/25/33MHz

**Chip Set** VLSI Max. Onboard DRAM 32MB Cache None **BIOS** Award

**Dimensions** 220mm x 220mm

I/O Options None **NPU Options** 4167



CONNECTIONS					
Purpose	Location	Purpose	Location		
External battery	J3	Turbo LED	J6		
Speaker	J4	Power LED & keylock	J7		
Turbo switch	J5	Reset switch	JP1		

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USER CONFIGURABLE SETTINGS					
Function	Jumper	Position			
í CMOS memory normal operation	JP2	pins 3 & 4 closed			
CMOS memory clear	JP2	pins 1 & 2 closed			

DRAM CONFIGURATION					
Size	Bank 0	Bank 1			
1MB	(4) 256K x 9	NONE			
2MB	(4) 256K x 9	(4) 256K x 9			
4MB	(4) 1M x 9	NONE			
5MB	(4) 256K x 9	(4) 1M x 9			
8MB	(4) 1M x 9	(4) 1M x 9			
16MB	(4) 4M x 9	NONE			
20MB	(4) 1M x 9	(4) 4M x 9			
32MB	(4) 4M x 9	(4) 4M x 9			

CPU TYPE CONFIGURATION							
Type	OSC1 (MHz)	JP3	JP4	JP5			
80486SX-16	33MHz	pins 2 & 3 closed	pins 1 & 2 closed	Open			
80486SX-20	40MHz	pins 2 & 3 closed	pins 1 & 2 closed	Open			
80486SX-25	50MHz	pins 2 & 3 closed	pins 1 & 2 closed	Open			
80486DX-25	50MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed			
80486DX-33	66MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed			