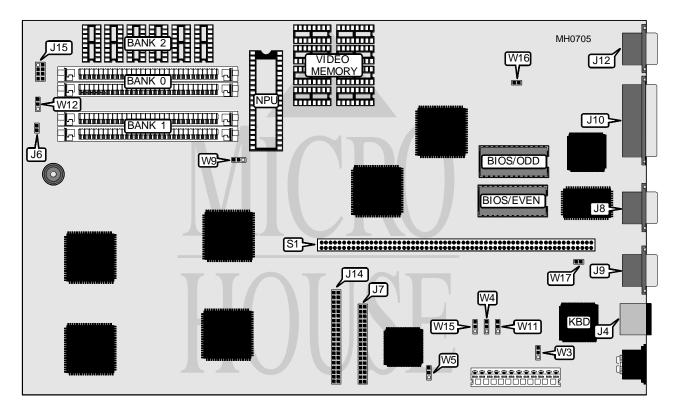
WESTERN DIGITAL CORPORATION W D 2 8 6 - L P M

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options

80286 6/12.5/16MHz Faraday 4MB None Phoenix 330mm x 198mm Parallel port, serial port (2), bus mousse port, floppy drive interface, video interface, IDE interface, proprietary bus expansion slot 80287

NPU Options



CONNECTIONS					
Purpose	Location	Purpose	Location		
Bus mouse port	J4	Parallel port	J10		
Reset switch	J6	VGA interface	J12		
Floppy drive interface	J7	IDE interface	J14		
Serial port 2	J8	IDE interface LED/Keylock	J15		
Serial port 1	19	Local bus expansion slot	\$1		

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Monitor type select color	W3	pins 2 & 3 closed		
Monitor type select monochrome	W3	pins 1 & 2 closed		
í Floppy drive interface enabled	W4	pins 2 & 3 closed		
Floppy drive interface disabled	W4	pins 1 & 2 closed		
í Factory configured - do not alter	W5	pins 1 & 2 closed		
í NPU speed select 6MHz	W9	pins 2 & 3 closed		
NPU speed select 12.5MHz	W9	pins 1 & 2 closed		
NPU speed select 16MHz	W9	pins 1 & 2 closed		
í Video port (J12) enabled	W11	pins 2 & 3 closed		
Video port (J12)disabled	W11	pins 1 & 2 closed		
í IDE interface LED enabled	W12	pins 2 & 3 closed		
IDE interface LED disabled	W12	pins 1 & 2 closed		
í Factory configured - do not alter	W15	pins 2 & 3 closed		
í Mouse port interrupt enabled (IRQ12)	W16	Closed		
Mouse port interrupt disabled (IRQ12)	W16	Open		
í Factory configured - do not alter	W17	Open		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(2) 256K x 9	(2) 256K x 9		
2MB	(2) 1M x 9	NONE		
4MB	(2) 1M x 9	(2) 1M x 9		
Note: Bank 2 is an optional 128KB soldered onto the board on the 12MHz version only.				