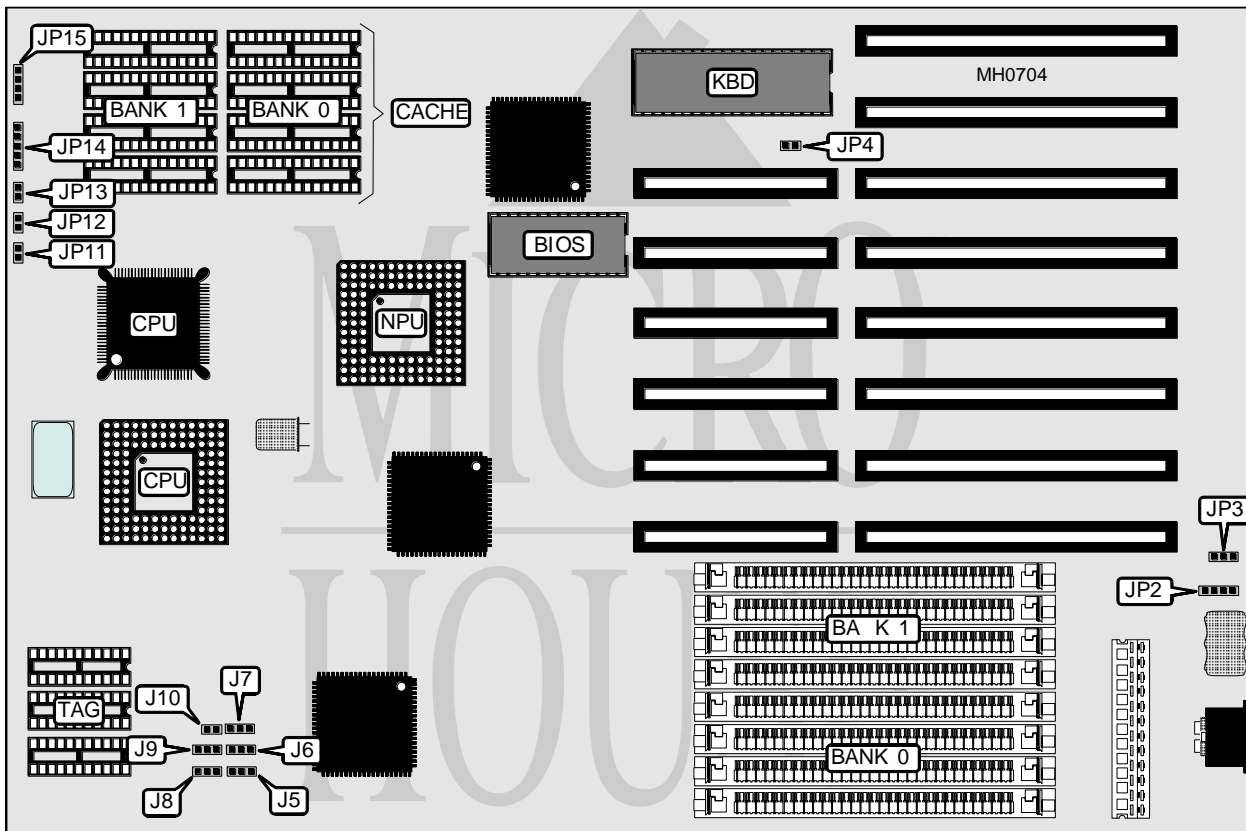


AMERICAN DIGICOM CORPORATION

DIGIS - 386 L

Processor	80386DX
Processor Speed	25/33/40MHz
Chip Set	ATMEL/MIC/OPTI
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Turbo switch	JP13
Reset switch	JP11	Power LED & keylock	JP14
Turbo LED	JP12	Speaker	JP15

Continued on next page . . .

AMERICAN DIGICOM CORPORATION

DIGIS - 386 L

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP3	pins 2 & 3 closed
CMOS memory clear	JP3	pins 1 & 2 closed
í Monitor type select color	JP4	Closed
Monitor type select monochrome	JP4	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(3) 64K x 1
64KB	(4) 8K x 8	(4) 8K x 8	(3) 64K x 1
128KB	(4) 32K x 8	NONE	(3) 64K x 1
256KB	(4) 32K x 8	(4) 32K x 8	(3) 64K x 1

CACHE JUMPER CONFIGURATION						
Size	J5	J6	J7	J8	J9	J10
32KB	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2	Open
64KB	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	Open
128KB	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3	Closed
256KB	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3	Closed

Note: Pins designated should be in the closed position.