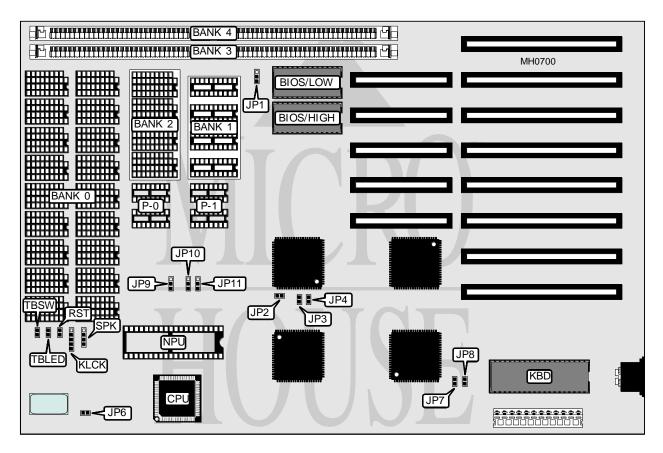
BIOSTAR MICROTECH INTERNATIONAL CORPORATION 286 SYSTEM BOARD

Processor 80286 **Processor Speed** 10/12MHz **Chip Set** VLSI Max. Onboard DRAM 4MB Cache None

BIOS AMI/Award/Phoenix 330mm x 218mm **Dimensions**

I/O Options None **NPU Options** 80287



CONNECTIONS				
Purpose	Location	Purpose	Location	
Power LED & keylock	KLCK	Turbo LED	TBLED	
Reset switch	RST	Turbo switch	TBSW	
Speaker	SPK			

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Location		
í BIOS type select 27128	JP1	pins 2 & 3 closed		
BIOS type select 27256	JP1	pins 1 & 2 closed		
í CPU slow speed select 8MHz	JP6	Closed		
CPU slow speed select 6MHz	JP6	Open		
í Monitor type select color	JP7	Closed		
Monitor type select monochrome	JP7	Open		
í DRAM speed select 0 wait states	JP8	Closed		
DRAM speed select 1 wait state	JP8	Open		

DRAM CONFIGURATION							
Size	Bank 0	Bank 1	P-1	Bank 2	P-2	Bank 3	Bank 4
512KB ¹	(18) 41256	NONE	NONE	NONE	NONE	NONE	NONE
512KB ¹	NONE	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB ²	NONE	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB ¹	(18) 41256	NONE	NONE	(4) 4464	(2) 4164	NONE	NONE
640KB ¹	NONE	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
640KB ²	(18) 4164	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB ²	NONE	NONE	NONE	(4) 4464	(2) 4164	(2) 256K x 9	NONE
1MB ¹	(18) 41256	NONE	NONE	(2) 44256	(4) 41256	NONE	NONE
1MB ¹	(18) 41256	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
1MB ²	NONE	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB ¹	(18) 411000	NONE	NONE	NONE	NONE	NONE	NONE
2MB ²	NONE	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB ¹	(18) 411000	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB ²	NONE	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9

Note ¹: Set JP9, JP10, and JP11 pins 2 & 3 closed. Note ²: Set JP9, JP10, and JP11 pins 1 & 2 closed.

DRAM JUMPER CONFIGURATION					
Size	JP2	JP3	JP4		
512KB	Closed	Closed	Closed		
640KB	Closed	Closed	Open		
1MB	Closed	Open	Closed		
2MB	Closed	Open	Open		
4MB	Open	Closed	Closed		