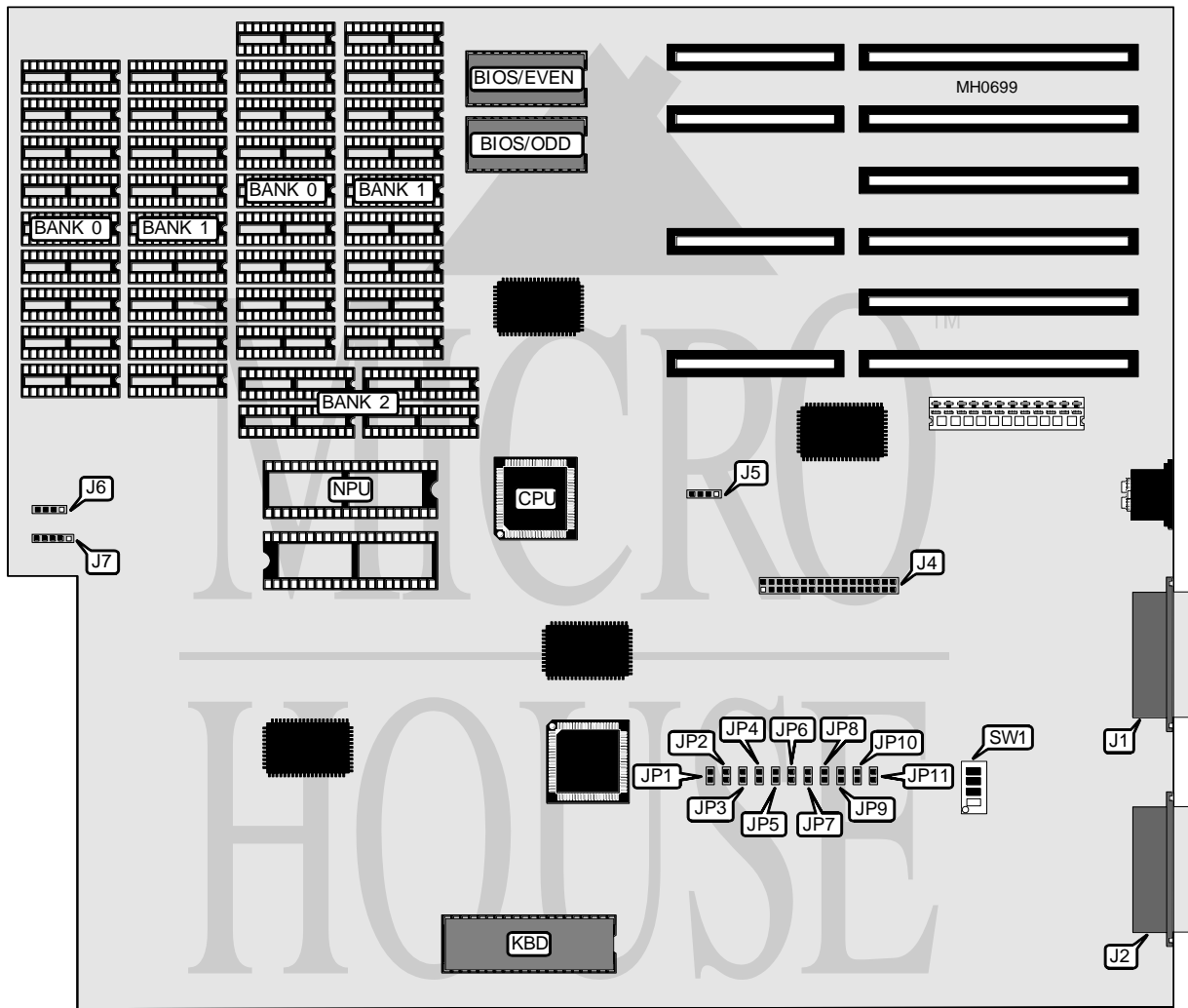


HYUNDAI ELECTRONICS, INC. SUPER 286C (REV. H)

Processor	80286
Processor Speed	10MHz
Chip Set	VLSI
Max. Onboard DRAM	1MB
Cache	None
BIOS	Award
Dimensions	355mm x 304mm
I/O Options	Floppy drive interface, parallel port, serial port
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port	J1	Speaker	J6
Parallel port	J2	Power LED & keylock	J7
External battery	J5		

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SUPER 286C (REV. H)

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	JP3	Open
í Factory configured - do not alter	JP6	Open
í Floppy drive interface enabled	JP11	Closed
Floppy drive interface disabled	JP11	Open
í Factory configured - do not alter	SW1/3	Off
í Monitor type select color	SW1/4	On
Monitor type select monochrome	SW1/4	Off

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
512KB	(18) 41256	NONE	NONE
640KB	(18) 41256	NONE	(4) 4464
1MB	(18) 41256	(18) 41256	NONE

DRAM JUMPER CONFIGURATION				
Size	Base memory	Extended memory	SW1/1	SW1/2
512KB	512KB	0KB	Off	On
640KB	640KB	0KB	On	Off
1MB	512KB	512KB	Off	On
1MB	640KB	384KB	Off	Off

PARALLEL PORT CONFIGURATION				
Setting	JP7	JP8	JP9	JP10
LPT1	Closed	Open	Closed	Open
LPT2	Open	Closed	Open	Closed
Disabled	Open	Open	Open	Open

SERIAL PORT CONFIGURATION				
Setting	JP1	JP2	JP4	JP5
COM1/IRQ4	Closed	Open	Closed	Open
COM2/IRQ3	Open	Closed	Open	Closed
Disabled	Open	Open	Open	Open