HYUNDAI ELECTRONICS, INC.
SUPER 286C (REV. C THRU F8)

Processor	80286
Processor Speed	10MHz
Chip Set	VLSI
Max. Onboard DRAM	1MB
Cache	None
BIOS	Award
Dimensions	355mm x 304mm
I/O Options	Serial port, parallel port, floppy drive interface
NPU Options	80287
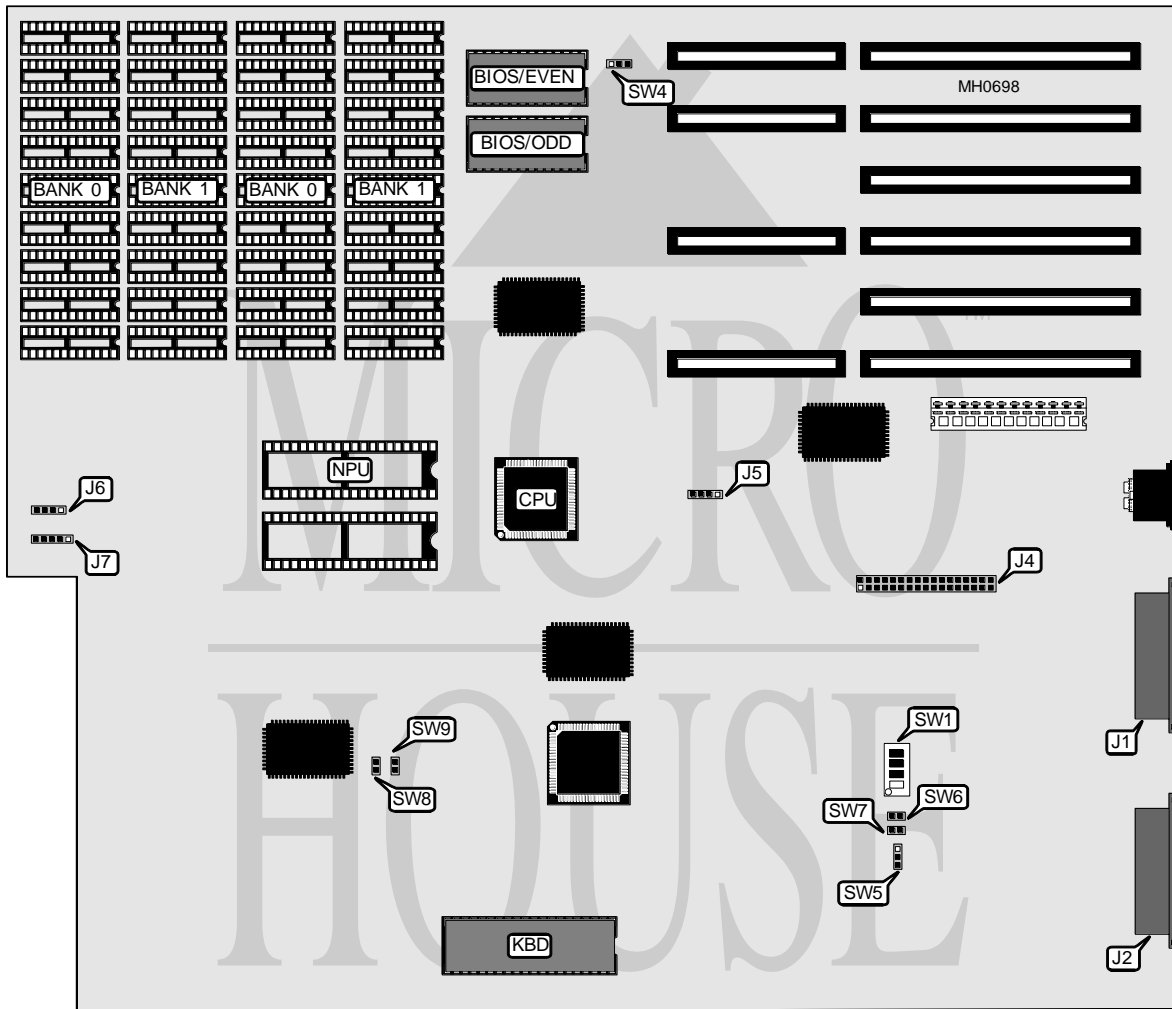
CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port	J1	Speaker	J6
Parallel port	J2	Power LED & keylock	J7
External battery	J5		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í CPU speed select 10MHz	SW1/3	On
CPU speed select 8MHz	SW1/3	Off
í Monitor type select color	SW1/4	On
Monitor type select monochrome	SW1/4	Off
í Factory configured - do not alter	SW2	pins 1 & 2 closed
í CS ROM	SW3	pins 1 & 2 closed
XMEMR	SW3	pins 2 & 3 closed
í BIOS type select 27256	SW4	pins 2 & 3 closed
BIOS type select 27128	SW4	pins 1 & 2 closed
í Floppy drive mode select single speed	SW5	pins 1 & 2 closed
Floppy drive mode select dual speed	SW5	pins 2 & 3 closed
í COM1 enabled (IRQ4)	SW6 & SW9	Closed
COM1 disabled	SW6 & SW9	Open
í Factory configured - do not alter	SW7	Closed
í Factory configured - do not alter	SW8	Closed
Note: The locations of SW2 & SW3 are unidentified.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(18) 41256	NONE
640KB	(18) 41256	(18) 4164
1MB	(18) 41256	(18) 41256

DRAM SWITCH CONFIGURATION		
Size	SW1/switch 1	SW1/switch 2
512KB	Off	On
640KB	On	Off
1MB	Off	Off