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S U P E R - 3 8 6 T

Processor	80386DX
Processor Speed	33MHz
Chip Set	VLSI
Max. Onboard DRAM	8MB
Cache	64/256KB
BIOS	AMI
Dimensions	355mm x 304mm
I/O Options	32-bit external memory card slot, serial port (2), parallel port
NPU Options	80387/3167
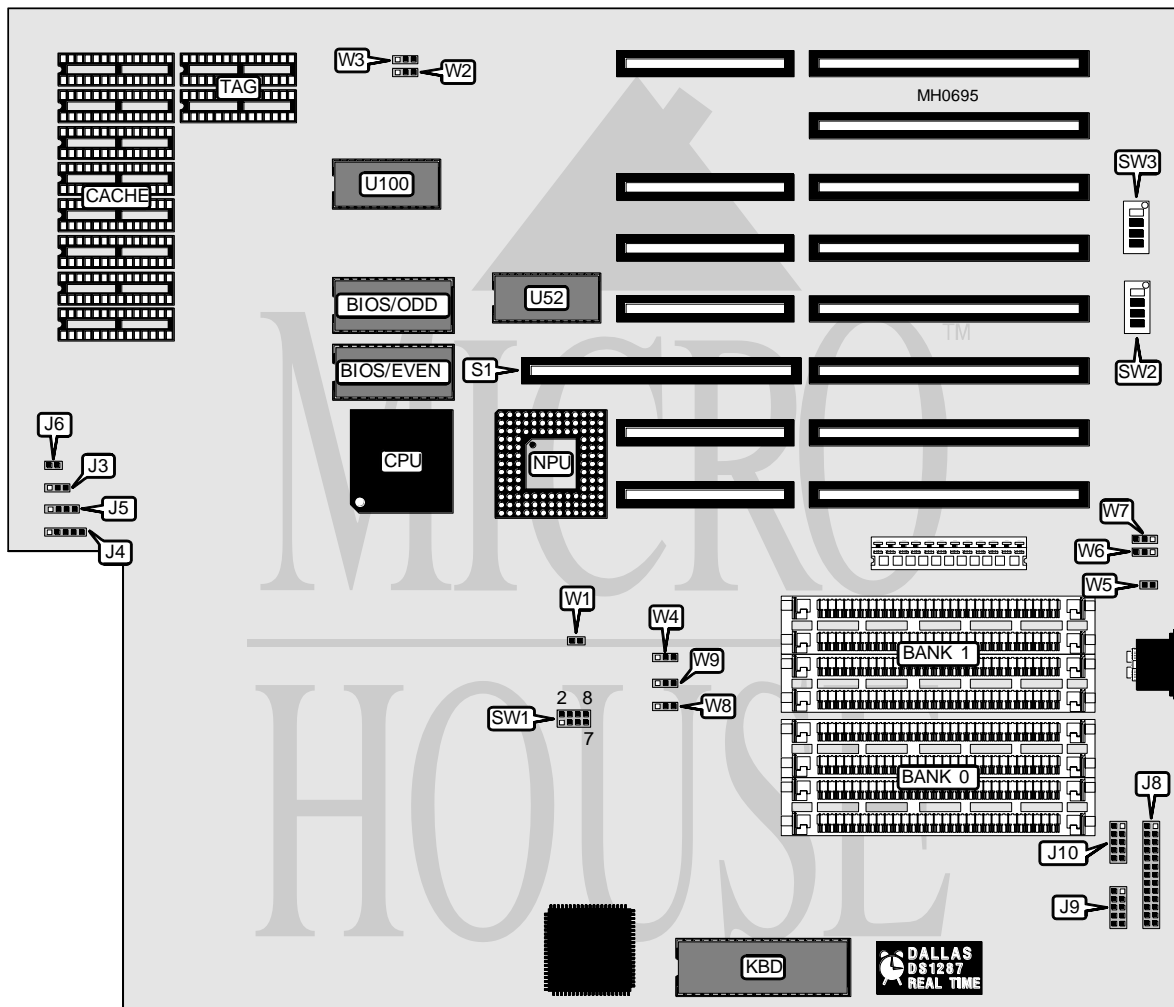
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J3	Parallel port	J8
Power LED & keylock	J4	Serial port 1	J9
Speaker	J5	Serial port 2	J10
Reset switch	J6	32-bit external memory card	S1

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Serial port (J9) select COM1/3F8h	SW2/1	Off
Serial port (J9) disabled	SW2/1	On
í Serial port (J10) select COM2/2F8h	SW2/2	Off
Serial port (J10) disabled	SW2/2	On
í Parallel port (J8) enabled	SW2/3	Off
Parallel port (J8) disabled	SW2/3	On
í Parallel port (J8) select LPT1/378h	SW2/4	Off
Parallel port (J8) select LPT2/278h	SW2/4	On
í Serial port (J9) interrupt select IRQ4	SW3/1	Off
Serial port (J9) interrupt select none	SW3/1	On
í Serial port (J10) interrupt select IRQ3	SW3/2	Off
Serial port (J10) interrupt select none	SW3/2	On
í DMA clock speed select fast	W1	Closed
DMA clock speed select slow	W1	Open
í Factory configured - do not alter	W4	pins 2 & 3 closed
í Factory configured - do not alter	W5	N/A
í CPU speed select switchable from keyboard	W6	pins 2 & 3 closed
CPU speed select forced slow	W6	pins 1 & 2 closed
í Monitor type select color	W7	pins 2 & 3 closed
Monitor type select monochrome	W7	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	PAL (U52)	PAL (U100)
1MB	(4) 256K x 9	NONE	B	B
2MB	(4) 256K x 9	(4) 256K x 9	B	B
4MB	(4) 1M x 9	NONE	A	A
5MB	(4) 256K x 9	(4) 1M x 9	C	C
8MB	(4) 1M x 9	(4) 1M x 9	A	A

DRAM JUMPER/SWITCH CONFIGURATION						
Size	SW1/1 & 2	SW1/3 & 4	SW1/5 & 6	SW1/7 & 8	W8	W9
1MB	Off	Off	Off	On	2 & 3	2 & 3
2MB	Off	Off	Off	Off	2 & 3	2 & 3
4MB	Off	Off	Off	On	1 & 2	1 & 2
5MB	Off	Off	Off	Off	2 & 3	1 & 2
8MB	Off	Off	Off	Off	1 & 2	1 & 2
Note: Pins designated should be in the closed position.						

CACHE CONFIGURATION		
Size	Bank 0	TAG
64KB	(8) 16K x 4	(2) 16K x 4
256KB	(8) 64K x 4	(2) 64K x 4
Note: The location of tag chips are unidentified.		

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CACHE JUMPER CONFIGURATION		
Size	W2	W3
64KB	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed

PARALLEL PORT CONFIGURATION		
IRQ	SW/3	SW3/4
IRQ7	Off	On
IRQ5	On	Off
None	Off	Off