UNIDENTIFIED
H T 1 2 2 8 6

Processor	80286
Processor Speed	12/16MHz
Chip Set	Headland
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80287
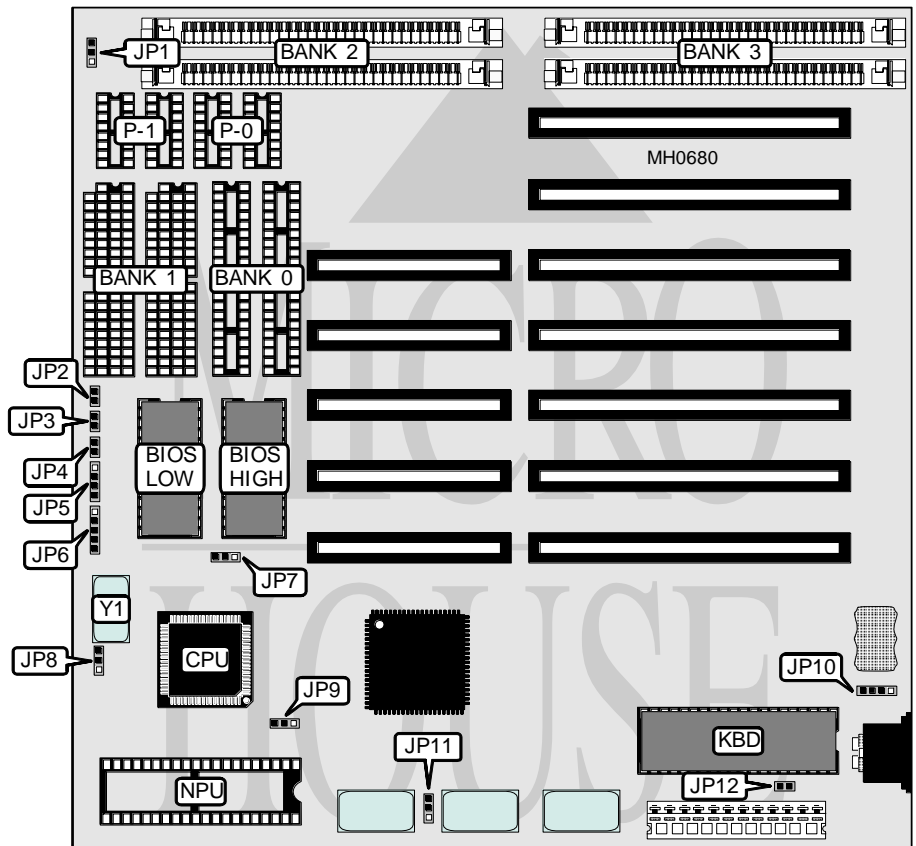
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	JP2	Speaker	JP5
Turbo LED	JP3	Power LED & keylock	JP6
Reset switch	JP4	External battery	JP10

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HT 12 286

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	N/A
í BIOS type select 27512	JP7	pins 1 & 2 closed
BIOS type select 27256	JP7	pins 2 & 3 closed
í NPU mode select synchronous with CPU	JP8	pins 2 & 3 closed
NPU mode select synchronous with oscillator installed at Y1	JP8	pins 1 & 2 closed
í NPU speed select source/1	JP9	pins 1 & 2 closed
NPU speed select source/3	JP9	pins 2 & 3 closed
í CPU speed select 16MHz	JP11	pins 2 & 3 closed
CPU speed select 12MHz	JP11	pins 1 & 2 closed
í Monitor type select color	JP12	Closed
Monitor type select monochrome	JP12	Open

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 41256	NONE	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9