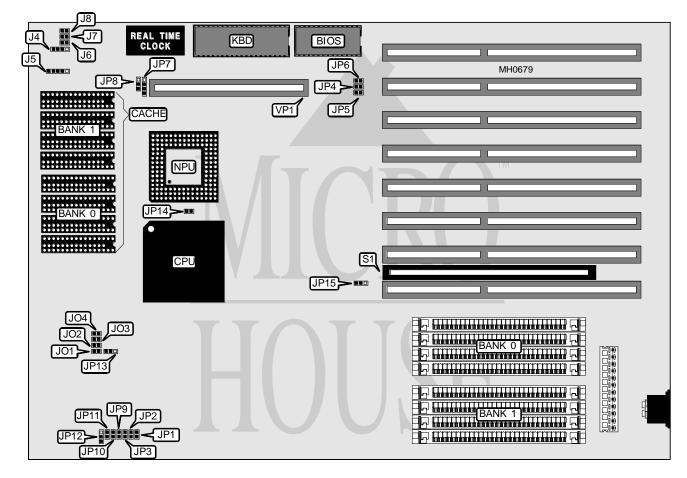
## ADVANCED INTEGRATION RESEARCH, INC. 486EO-50/33/25/20 EISA

Processor	804865X/804875X/80486DX/ODP4865X/80486DX2
Processor Speed	20/25/33/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	64/128/256/512KB
BIOS	AMI/Phoenix
Dimensions	309mm x 331mm
I/O Options	32-bit external memory card slot, 32-bit external video card slot
NPU Options	4167



CONNECTIONS					
Purpose Location Purpose Location					
Speaker	J4	Turbo LED	J8		
Power LED & Keylock	J5	32-bit external memory card	S1		
Reset switch	J6	32-bit external video card	VP1		
Turbo switch	J7				

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## ADVANCED INTEGRATION RESEARCH, INC. 486EO-50/33/25/20 EISA

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USER CONFIGURABLE SETTINGS				
Function Jumper Position				
í Factory configured - do not alter	JO3	Open		
í Factory configured - do not alter	JO4	Open		
í Monitor type select monochrome	JP6	Open		
Monitor type select color	JP6	Closed		

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3		
4MB	(4) 1M x 9	NONE	NONE	NONE		
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE		
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE		
16MB	(4) 1M x 9					
16MB	(4) 4M x 9	NONE	NONE	NONE		
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE		
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE		
28MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9		
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE		
36MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	NONE		
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9		
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE		
52MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9		
64MB	(4) 4M x 9					
64MB	(4) 16M x 9	NONE	NONE	NONE		
128MB	(4) 16M x 9	(4) 16M x 9	NONE	NONE		
192MB	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9	NONE		
256MB	(4) 16M x 9					
Note: Banks 2 & 3 are used when a 32-bit external memory card is installed at S1.						

	CACHE CONFIGURATION	
Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8
512KB	(4) 128K x 8	NONE

	CACHE JUMPER CONFIGURATION						
Size	JP1	JP2	JP3	JP9	JP10	JP11	JP12
64K	Open	Open	Open	Open	Open	Open	2&3
128K	Closed	Open	Open	Closed	Open	Open	1&2
256K	Closed	Closed	Open	Closed	Open	Closed	2&3
512K	Closed	Closed	Closed	Closed	Closed	Closed	1 & 2
Note: Pins designated should be in the closed position.							

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## ADVANCED INTEGRATION RESEARCH, INC. 486E0-50/33/25/20 EISA

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CPU TYPE CONFIGURATION				
Туре	JP7	JP8		
80486SX	pins 2 & 3 closed	open		
80487SX	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed		
80486DX	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed		
ODP486SX	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed		
80486DX2	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed		

			CPU SPEED CO	NFIGURATION			
Speed	JO1	JO2	JP4	JP5	JP13	JP14	JP15
20MHz	Open	Closed	Open	Open	2&3	Closed	2&3
25MHz	Open	Open	Open	Open	2&3	Closed	1&2
33MHz	Open	Closed	Open	Closed	2&3	Open	1&2
50iMHz	Open	Open	Open	Open	2&3	Closed	1&2
50MHz	Open	Open	Closed	Closed	1&2	Open	1&2
66iMHz	Open	Closed	Open	Closed	2&3	Open	1&2